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99.5.		MK200S/300S/1000S 가	V3.0
2000.4.		MK80S 가	V4.0

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[] Handy Loader Code 1

1

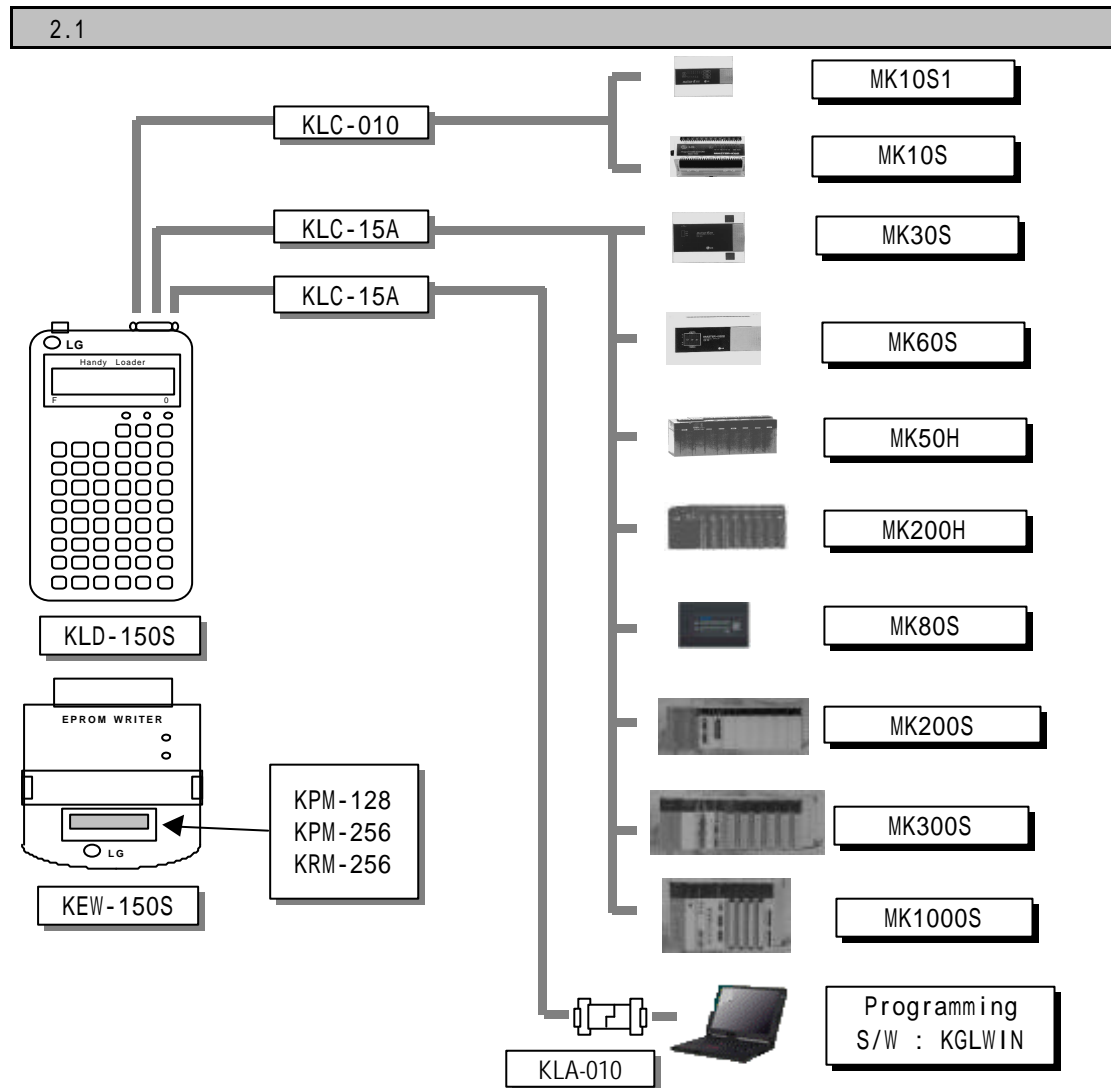
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1.1

KLD-150S MASTER-K Series
Read/Write , Test ,
,ROM Write .

1.2

- 1) MASTER-K Series
MASTER-K Series .
- 2) RUN Edit가
CPU RUN Program Edit가 가
(MK80S/200S/300S/1000S)
- 3) Handheld 가
- 4) Monitoring
가 가 .
- 5) EPROM Writer (KEW-150S)
KEW-150S EPROM Read/Write가 가 .
- 6) LCD back light
LCD Back light On/Off .
- 7) Buzzer
5 .



2.2

KLD-150S		LCD (1.5m) (KLC-15A)
KPM-128 KRM-256 KPM-256		EPROM(12.5V) Pack RAM Pack
KEW-150S	EPROM Writer	EPROM Read/Write/Verify
KLC-010		MASTER - K10S/K10S1 Loader cable ()
KLA-010		9:9 (:), Pin 2,3 Parallel ()

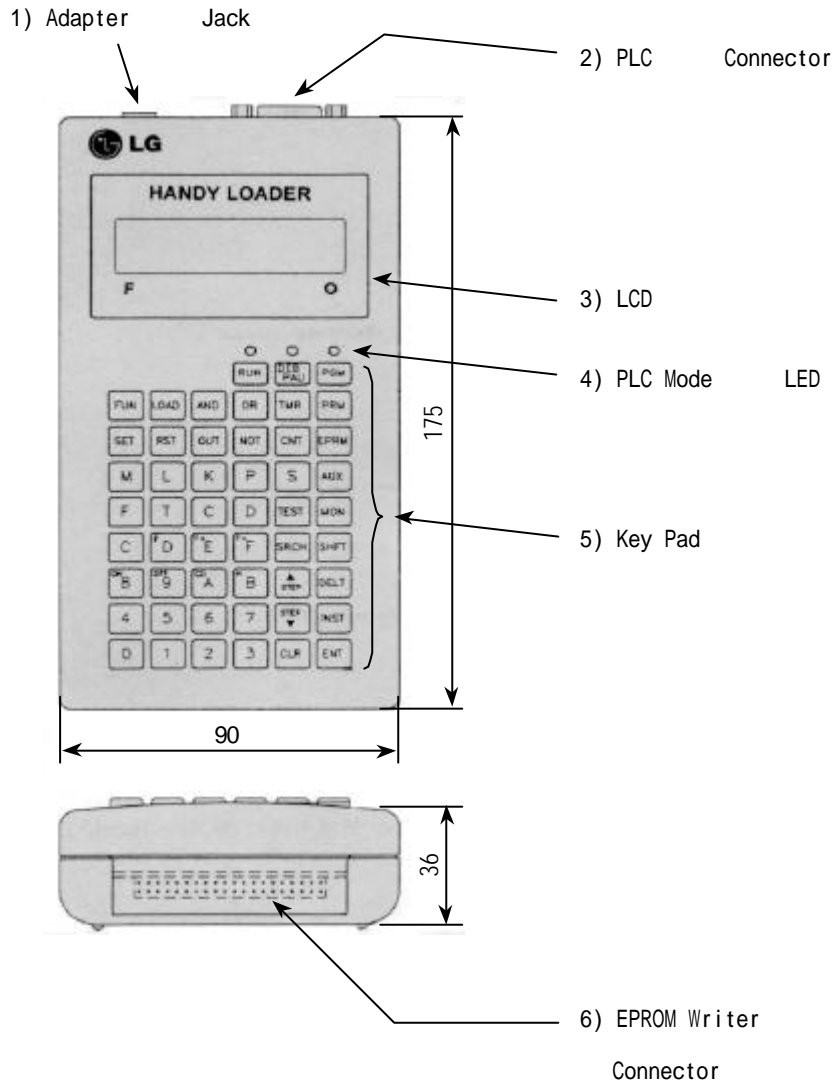
3.1

	-10 ~ 50
	0 ~ 40
	5 ~ 95% ()
	Gas가
	90W × 175H × 36D [mm]
	420 g

3.2

PLC	MASTER-K
	PLC (DC 5V 0.6A)
PLC	Cable (Handheld) : RS-232C, 9.6Kbps (K10S, K30S, K60S) 38.4Kbps (K200S, K300S, K1000S)
	16 , 2 Dot matrix LCD LCD : Key On/Off 가 Key 10 Off
Key Board	Mode LED . 3 Mode Key. Loader 48 Key. Key Buzzer Buzzer 1. Error Key 2. On/Off 가
Program	On-Line : PLC Program
EPROM	16K, 32K Byte EPROM

(:mm)



1) Adapter Jack
 Loader PLC Adapter Cord Jack . (DC 5V)

2) PLC Connector
 PLC Loader Cable Connector .

3) LCD
 16 , 2 Dot matrix LCD.
 Back-Light가 가 .

4) PLC mode LED
 PLC Mode LED PLC
 RUN, PROGRAM. PAUSE, DEBUG Mode .

5) Key Board
 .

6) EPROM Writer Connector
 KEW - 150S(Optional) Connector EPROM
 , , .

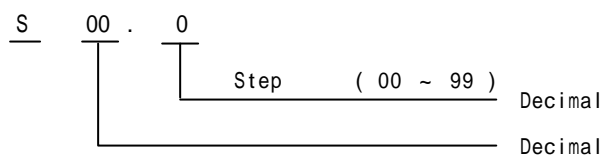
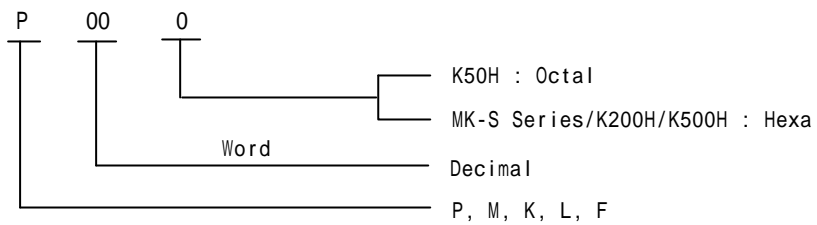
7) Key Board

Key		
Mode Key	RUN	PLC Run Mode .
	DEB /PAU	RUN Mode Pause Mode Program Mode Debug Mode .
	PGM	PLC Program Mode .
Key	SHFT	Key 2가 . Key .
Key	ENT	Program PLC Program Data (D, T/C ,) .
	CLR	가 . SHFT+CLR (Clear)
	TEST	T/C D Register Word .
	SRCH	Program , Program (Search)
	DELT	Program Step .
	INST	Program Step .
	STEP ▼	Program Step Monitor , Card . Address가 Setting Address Program .
Key	▲ STEP	Program Step Monitor , Card . Address가 Setting Address Program .
	LOAD	LOAD, LOAD NOT, AND LOAD, OR LOAD .
	AND	AND, AND NOT, AND LOAD .
	OR	OR, OR NOT, OR LOAD .
Key	NOT	LOAD NOT, AND NOT, OR NOT, NOT .

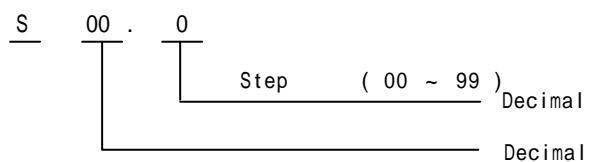
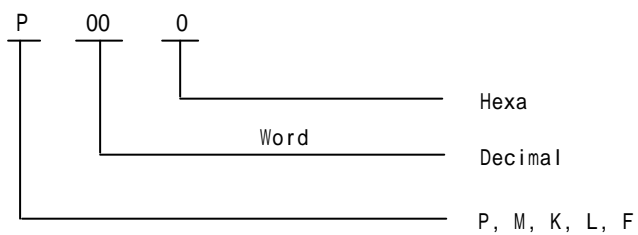
()		
Key		
Key	OUT	OUT
	SET	SET
	RST	RST (Reset)
	FUN	.
	TMR	(TON, TOFF, TMR, TMON, TRTG)
	CNT	(CTU, CTD, CTUD, CTR)
Key	P	()
	M	()
	K	()
	L	()
	T	
	C	
	F	()
	S	
	D	
Key	0 ~ 9	Addresss, , Data Register
	A ~ F	.
Key	PRM	Parameter .
	EPRM	User Program EPROM Writer RD/WR .
	ON (8)	On .
	OFF (9)	Off .
	CD (A)	P, L, M, K, F, S .
	H (B)	16 . (Hexa)
	• (-) (C)	(-) . (Timer) Step Controller S .
	# (D)	Data Register .
	F+ (E)	Function Number 가 .
	F- (F)	Function Number .
	AUX	.
	MON	.

3.4 Word,

1) K10S1 ~ K500H







2) MK1000H, 1000S/300S/200S/80S



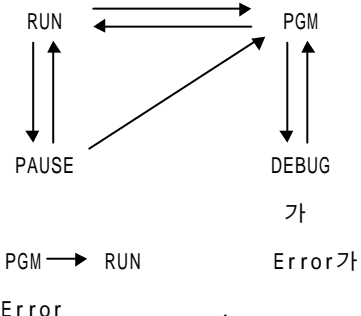
3.5 Mode

MASTER - K Series 4가 Mode가 .

Program Mode ()
 RUN Mode ()
 Debug Mode ()
 Pause Mode ()

1) Mode

KLD-150S Mode Mode
 Mode LED가 .

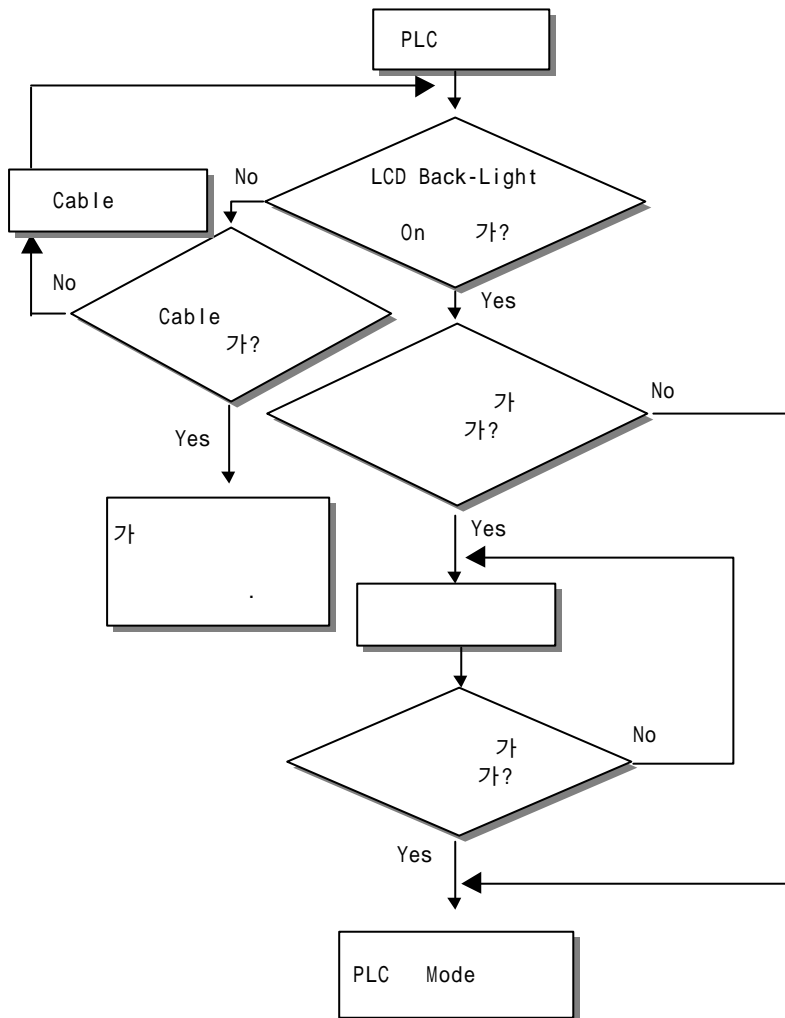
Mode	Mode Key	LED
PGM (Program Mode)		PGM LED
RUN (RUN Mode)		RUN LED
DEB (Debug Mode)		PGM + PAU
PAU (Pause Mode)		RUN + PAU

2) Mode

	Mode		
KLD-150S Handy Loader	RUN / PAUSE	, Card On/Off, Card Timer/Counter Operand Scan	
	PGM RUN PAUSE	EPROM Writer EPROM Read/Write/Check/Verify	
	PGM RUN PAUSE	Power On Mode Change Password	
	PGM	, Card, On/Off, Card Timer/Counter Operand Program Operand I/O table Latch area HSC (High Speed Counter)	
	DEBUG	Trace Run (1 Step) Step Break Run () Scan Break Run (Scan) Value Break Run (Card가)	

4.1 On (On Line)

4.1.1 Flow Chart

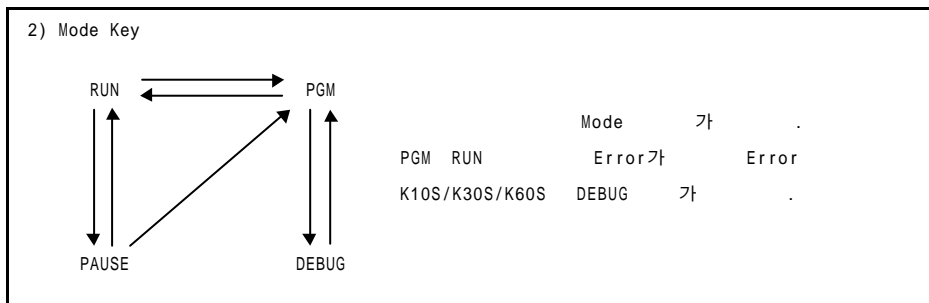


4.1.2

Mode				
RUN/PAU	PGM	DEB		
MASTER-K (PLC) CPU KLD-150S가			1) Password Password '0000' 2) PLC 'Error'가 Password Error	
LOADER			KEY	
<div> <div>H A N D Y L O A D E R</div> <div>X X / X X / X X V E R X . X</div> </div> <div> <div>1 . P R O G R A M M E R</div> <div>2 . E P R O M W R I T E R</div> </div> <div> <div>* S e l f - t e s t i n g *</div> <div>* P l e a s e w a i t ! *</div> </div> <div> <div>K 2 0 0 S A V X . X</div> <div>* P A S S W O R D : . . . *</div> </div> <div> <div>K 2 0 0 S A V X . X</div> <div># # P R O G R A M # #</div> </div> <div> <div>0 0 0 0 0</div> <div></div> </div>			<div> <div>1</div> <div>1</div> <div>2</div> </div> <div> KEW - 150S가 CPU EPROM Writer </div> <div> Password Test Password가 (Password가 가 .) CLR </div> <div> PLC CPU 1 Version "XX" O/S Version , , Version No. KEW - 150S가 CPU EPROM Writer Test Password가 (PLC Type) PLC KLD - 150S PLC CPU (RUN/PAU, PROGRAM, DEBUG) KLD - 150S Key </div>	

4.2 MODE

KLD-150S		Mode Key	PLC
1) Mode Key			
LOADER		KEY	
<div>##PROGRAM##</div> <div>MODE</div>		PGM	PGM Key PLC
<div>##RUN##</div> <div>MODE</div>		RUN	RUN Key PLC Run
<div>##PAUSE##</div> <div>MODE</div>		DEB PAU	DEB PAU Key Run Pause
<div>##PROGRAM##</div> <div>MODE</div>		PGM	Debug
<div>##DEBUG##</div> <div>MODE</div>		DEB PAU	



4.3

CPU .(.)

1.LATCH AREA	Latch
2.WDT TIME	Watchdog Timer
3.ERROR MODE	Error
4.I/O TABLE	I/O
5.PASSWORD	Password
6.HS LINK PRM	
7.HS LINK ENABLE	가/
8.DEBUG OUTPUT	/
9.INTERRUPT	
10.PDI SLOT SET	
11.FORCE ON/OFF	On/Off
12.FORCE ENABLE	On/Off /
13.REMOTE CONNECT	Remote
14.REMOTE DISCONNECT	Remote
15.TIMER SET	
16.REMOTE ACCESS	/
17.VERSION NO	Version number
18.COMMUNICATION	
19.COMMUNICATION	/
20.PID	PID
21.PULSE OUTPUT	
22.PULSE CATCH	
23.INPUT FILTER	Input Filter
24.ANALOGUE PARAMETER	
	MK80S

4.3.1 Latch Area

Mode				
RUN/PAU	PGM	DEB		
✕		✕	K10S/K60S/K80S/K200S/K300S/K1000S 가	
LOADER			KEY	
PLC PARAMETER 1. LATCH AREA			PRM	
LATCH L * * * - L * * *			ENT	
LATCH 100ms T0144 - T0191			STEP	Latch()
LATCH 100ms T0060 - T0191			6 0 ENT	100ms Default
LATCH 100ms T0060 - T0191			ENT	T0060
LATCH 10ms T0240 - T0255			STEP	10ms Default
LATCH C0192 - C0255			STEP	
LATCH D3500 - D4500			STEP	D (Data Device)
LATCH S80 - S99			STEP	S (Step Controller)
LATCH M * * * - M * * *				M (Auxiliary Memory)

1) Device	ENT	Key	가
2) STEP			
3) CLR			

4.3.2 Watchdog Time

Mode																																																																																																																																																																																																																																																						
RUN/PAU	PGM	DEB																																																																																																																																																																																																																																																				
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LOADER			KEY																																																																																																																																																																																																																																																			
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1) 20 (200ms) ~ 600 (6000ms)
(K200S, K300S, K1000S)

4.3.3 Error Mode

Mode																																																																					
RUN/PAU				PGM				DEB																																																													
x								x																																																													
Fuse, , Error																										K80S/K200S/K300S/K1000S																																											
LOADER													KEY																																																								
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4.3.4 I/O Table

Mode				
RUN/PAU	PGM	DEB		
X		X	I/O Table	I/O

LOADER	KEY																																																																																																																																																																																																																																																													
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P	L	C	P	A	R	A	M	E	T	E	R																																																																																																																																																																																																																																																			
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5	.	P	A	S	S	W	O	R	D																																																																																																																																																																																																																																																					

1) I/O	.(K10/10S1/30S/60S/80S).
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4.3.5 Password

Mode			
RUN/PAU	PGM	DEB	
X		X	

LOADER	KEY																									
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>5</td><td>.</td><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R	5	.	P	A	S	S	W	O	R	D				
P	L	C	P	A	R	A	M	E	T	E	R															
5	.	P	A	S	S	W	O	R	D																	
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>5</td><td>.</td><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R	5	.	P	A	S	S	W	O	R	D			<div>ENT</div>	Password가 Password '1111' Password
P	L	C	P	A	R	A	M	E	T	E	R															
5	.	P	A	S	S	W	O	R	D																	
<table border="1"> <tr><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>O</td><td>L</td><td>D</td><td></td><td>:</td><td></td><td>.</td><td>.</td><td>.</td><td>.</td><td></td></tr> </table>	P	A	S	S	W	O	R	D						O	L	D		:			<div>1</div> <div>1</div> <div>1</div> <div>1</div>	Password Password '1111' Password
P	A	S	S	W	O	R	D																			
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<table border="1"> <tr><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>O</td><td>L</td><td>D</td><td></td><td>:</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>	P	A	S	S	W	O	R	D						O	L	D		:							Password가 가	Password ('0000' Password
P	A	S	S	W	O	R	D																			
	O	L	D		:																					
<table border="1"> <tr><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>N</td><td>E</td><td>W</td><td></td><td>:</td><td></td><td>.</td><td>.</td><td>.</td><td>.</td><td></td></tr> </table>	P	A	S	S	W	O	R	D						N	E	W		:			<div>1</div> <div>2</div> <div>3</div> <div>4</div>	Password ('1234')
P	A	S	S	W	O	R	D																			
	N	E	W		:																	
<table border="1"> <tr><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>V</td><td>E</td><td>R</td><td>I</td><td>F</td><td>Y</td><td>:</td><td></td><td>.</td><td>.</td><td>.</td></tr> </table>	P	A	S	S	W	O	R	D						V	E	R	I	F	Y	:		.	.	.	<div>1</div> <div>2</div> <div>3</div> <div>4</div>	Password 'NEW' Password
P	A	S	S	W	O	R	D																			
	V	E	R	I	F	Y	:		.	.	.															
<table border="1"> <tr><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td>V</td><td>E</td><td>R</td><td>I</td><td>F</td><td>Y</td><td>:</td><td></td><td></td><td></td><td></td></tr> </table>	P	A	S	S	W	O	R	D						V	E	R	I	F	Y	:						
P	A	S	S	W	O	R	D																			
	V	E	R	I	F	Y	:																			
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>5</td><td>.</td><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R	5	.	P	A	S	S	W	O	R	D				Password
P	L	C	P	A	R	A	M	E	T	E	R															
5	.	P	A	S	S	W	O	R	D																	

4.3.6 HS Link Parameter



1)MK80S/200S/300S/1000S

Mode																													
RUN/PAU	PGM	DEB																											
x		x																											
Data Link			MK80S/ K200S/K300S/K1000S																										
LOADER			KEY																										
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>.</td><td>L</td><td>A</td><td>T</td><td>C</td><td>H</td><td>A</td><td>R</td><td>E</td><td>A</td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R	1	.	L	A	T	C	H	A	R	E	A		[PRM] [STEP] x 5		
P	L	C	P	A	R	A	M	E	T	E	R																		
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<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>6</td><td>.</td><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>P</td><td>R</td><td>M</td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R	6	.	H	S	L	I	N	K	P	R	M		[ENT]		
P	L	C	P	A	R	A	M	E	T	E	R																		
6	.	H	S	L	I	N	K	P	R	M																			
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>P</td><td>R</td><td>M</td><td></td><td></td><td></td></tr> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>1</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			H	S	L	I	N	K	P	R	M				H	S	L	I	N	K	1						[STEP] or [STEP] [ENT]		
H	S	L	I	N	K	P	R	M																					
H	S	L	I	N	K	1																							
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>1</td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>U</td><td>N</td><td>I</td><td>T</td><td>:</td><td>M</td><td>K</td><td>S</td><td></td><td>F</td><td>n</td><td>e</td><td>t</td></tr> </table>			H	S	L	I	N	K	1						U	N	I	T	:	M	K	S		F	n	e	t	[STEP] or [STEP] [ENT]	
H	S	L	I	N	K	1																							
U	N	I	T	:	M	K	S		F	n	e	t																	
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>1</td><td>F</td><td></td><td></td><td></td><td></td></tr> <tr><td>S</td><td>L</td><td>O</td><td>T</td><td>:</td><td>0</td><td>S</td><td>F</td><td>_</td><td>S</td><td>T</td><td>:</td><td>0</td><td>0</td></tr> </table>			H	S	L	I	N	K	1	F					S	L	O	T	:	0	S	F	_	S	T	:	0	0	[1] [ENT] [2]
H	S	L	I	N	K	1	F																						
S	L	O	T	:	0	S	F	_	S	T	:	0	0																
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>1</td><td>F</td><td></td><td></td><td></td><td></td></tr> <tr><td>S</td><td>L</td><td>O</td><td>T</td><td>:</td><td>1</td><td>S</td><td>F</td><td>_</td><td>S</td><td>T</td><td>:</td><td>0</td><td>2</td></tr> </table>			H	S	L	I	N	K	1	F					S	L	O	T	:	1	S	F	_	S	T	:	0	2	[ENT]
H	S	L	I	N	K	1	F																						
S	L	O	T	:	1	S	F	_	S	T	:	0	2																
			[STEP] 5																										
			HS Link K200S : 1 ~ 2 K300S : 1 ~ 2 K1000S : 1 ~ 4																										
			HS Link Unit Type																										
			Slot Slot : 0 ~ 7 Self-ST : 0 ~ 63																										

LOADER	KEY																										
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>S</td><td>T</td><td>:</td><td>*</td><td>*</td><td></td><td>T</td><td>Y</td><td>P</td><td>E</td><td>:</td><td>*</td><td>*</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	S	T	:	*	*		T	Y	P	E	:	*	*	<div>1</div> <div>ENT</div> <div>STEP</div> <div>▼</div>	(0 ~ 63) Link Type Type : ST No Self Station No가 LS (Local Send 가) ST No Self Station No가 LS : Local Send (TX) LR : Local Receive (RX) RS : Remote Send (TX/RX) RR : Remote Receive (TX/RX)	
H	S	L	1	F	1	0	2	#	0	0																	
S	T	:	*	*		T	Y	P	E	:	*	*															
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>S</td><td>T</td><td>:</td><td>0</td><td>1</td><td></td><td>T</td><td>Y</td><td>P</td><td>E</td><td>:</td><td>L</td><td>R</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	S	T	:	0	1		T	Y	P	E	:	L	R	<div>ENT</div>		
H	S	L	1	F	1	0	2	#	0	0																	
S	T	:	0	1		T	Y	P	E	:	L	R															
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>I</td><td>D</td><td>:</td><td>0</td><td>0</td><td></td><td>C</td><td>Y</td><td>:</td><td>2</td><td>0</td><td>m</td><td>s</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	I	D	:	0	0		C	Y	:	2	0	m	s	<div>1</div> <div>ENT</div> <div>STEP</div> <div>▼</div>		
H	S	L	1	F	1	0	2	#	0	0																	
I	D	:	0	0		C	Y	:	2	0	m	s															
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>I</td><td>D</td><td>:</td><td>0</td><td>1</td><td></td><td>C</td><td>Y</td><td>:</td><td>5</td><td>0</td><td>m</td><td>s</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	I	D	:	0	1		C	Y	:	5	0	m	s	<div>ENT</div>	ID CY ID : Data Block ID (0~31) CY : (20ms, 50ms, 100ms, 200ms, 500ms, 1s, 5s, 10s)	
H	S	L	1	F	1	0	2	#	0	0																	
I	D	:	0	1		C	Y	:	5	0	m	s															
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>R</td><td>X</td><td>:</td><td>*</td><td>*</td><td>*</td><td>*</td><td></td><td>N</td><td>:</td><td>*</td><td>*</td><td>*</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	R	X	:	*	*	*	*		N	:	*	*	*	<div>D</div> <div>1</div> <div>0</div> <div>ENT</div> <div>4</div>		
H	S	L	1	F	1	0	2	#	0	0																	
R	X	:	*	*	*	*		N	:	*	*	*															
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>0</td></tr> <tr><td>R</td><td>X</td><td>:</td><td>D</td><td>0</td><td>0</td><td>1</td><td>0</td><td></td><td>N</td><td>:</td><td>0</td><td>0</td><td>4</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	0	R	X	:	D	0	0	1	0		N	:	0	0	4	<div>ENT</div>	
H	S	L	1	F	1	0	2	#	0	0																	
R	X	:	D	0	0	1	0		N	:	0	0	4														
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>1</td><td>F</td><td>1</td><td>0</td><td>2</td><td>#</td><td>0</td><td>1</td></tr> <tr><td>S</td><td>T</td><td>:</td><td>*</td><td>*</td><td></td><td>T</td><td>Y</td><td>P</td><td>E</td><td>:</td><td>*</td><td>*</td></tr> </table>	H	S	L	1	F	1	0	2	#	0	1	S	T	:	*	*		T	Y	P	E	:	*	*		Link Parameter CPU KGLWIN	
H	S	L	1	F	1	0	2	#	0	1																	
S	T	:	*	*		T	Y	P	E	:	*	*															

1) HS Link Parameter HS Link Enable 'No' 가

2)MK250H/500H/1000H

Mode																																
RUN/PAU	PGM	DEB																														
x		x																														
Link			K250H/K500H/K1000H																													
LOADER		KEY																														
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>.</td><td>L</td><td>A</td><td>T</td><td>C</td><td>H</td><td>A</td><td>R</td><td>E</td><td>A</td><td></td><td></td></tr> </table>		P	L	C	P	A	R	A	M	E	T	E	R			1	.	L	A	T	C	H	A	R	E	A			<div>PRM</div> <div>STEP  x 6</div>			
P	L	C	P	A	R	A	M	E	T	E	R																					
1	.	L	A	T	C	H	A	R	E	A																						
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>7</td><td>.</td><td>L</td><td>I</td><td>N</td><td>K</td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td></tr> </table>		P	L	C	P	A	R	A	M	E	T	E	R			7	.	L	I	N	K	C	O	N	T	R	O	L		<div>ENT</div>		
P	L	C	P	A	R	A	M	E	T	E	R																					
7	.	L	I	N	K	C	O	N	T	R	O	L																				
<table border="1"> <tr><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr> <tr><td>S</td><td>Y</td><td>N</td><td>C</td><td></td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td>N</td><td>O</td></tr> </table>			L	I	N	K	C	O	N	T	R	O	L			S	Y	N	C		E	N	A	B	L	E	:	N	O	<div>ENT 8</div>		
	L	I	N	K	C	O	N	T	R	O	L																					
S	Y	N	C		E	N	A	B	L	E	:	N	O																			
<table border="1"> <tr><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr> <tr><td>S</td><td>Y</td><td>N</td><td>C</td><td></td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td>Y</td><td>E</td><td>S</td></tr> </table>			L	I	N	K	C	O	N	T	R	O	L			S	Y	N	C		E	N	A	B	L	E	:	Y	E	S	<div>ENT</div>	
	L	I	N	K	C	O	N	T	R	O	L																					
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	L	I	N	K	C	O	N	T	R	O	L																					
W	R	I	T	E		P	R	O	T		:	N	O																			
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	L	I	N	K	C	O	N	T	R	O	L																					
W	R	I	T	E		P	R	O	T		:	Y	E	S																		
		<div>STEP  5</div>																														
		<div>PLC Scan Remote</div>																														
		<div>Host PC PLC Data Write Protection</div>																														

LOADER	KEY																																	
<table><tr><td></td><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr><tr><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td><td>P</td><td>R</td><td>O</td><td>T</td><td>:</td><td></td><td>N</td><td>O</td><td></td><td></td></tr></table>			L	I	N	K		C	O	N	T	R	O	L			A	C	C	E	S	S	P	R	O	T	:		N	O			<div>ON 8</div>	
		L	I	N	K		C	O	N	T	R	O	L																					
A	C	C	E	S	S	P	R	O	T	:		N	O																					
<table><tr><td></td><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr><tr><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td><td>P</td><td>R</td><td>O</td><td>T</td><td>:</td><td></td><td>Y</td><td>E</td><td>S</td><td></td></tr></table>			L	I	N	K		C	O	N	T	R	O	L			A	C	C	E	S	S	P	R	O	T	:		Y	E	S		<div>ENT</div>	Host PC가 PLC Data Read/Write Protection
		L	I	N	K		C	O	N	T	R	O	L																					
A	C	C	E	S	S	P	R	O	T	:		Y	E	S																				
<table><tr><td></td><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr><tr><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td></td><td>N</td><td>O</td><td></td></tr></table>			L	I	N	K		C	O	N	T	R	O	L			L	I	N	K		E	N	A	B	L	E	:		N	O		<div>ON 8</div>	
		L	I	N	K		C	O	N	T	R	O	L																					
L	I	N	K		E	N	A	B	L	E	:		N	O																				
<table><tr><td></td><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr><tr><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td></td><td>Y</td><td>E</td><td>S</td></tr></table>			L	I	N	K		C	O	N	T	R	O	L			L	I	N	K		E	N	A	B	L	E	:		Y	E	S	<div>ENT</div>	Data Link
		L	I	N	K		C	O	N	T	R	O	L																					
L	I	N	K		E	N	A	B	L	E	:		Y	E	S																			
<table><tr><td></td><td></td><td>L</td><td>I</td><td>N</td><td>K</td><td></td><td>C</td><td>O</td><td>N</td><td>T</td><td>R</td><td>O</td><td>L</td><td></td><td></td></tr><tr><td>S</td><td>E</td><td>L</td><td>F</td><td>_</td><td>S</td><td>T</td><td>.</td><td></td><td>N</td><td>O</td><td>:</td><td></td><td>h</td><td>0</td><td>0</td></tr></table>			L	I	N	K		C	O	N	T	R	O	L			S	E	L	F	_	S	T	.		N	O	:		h	0	0	<div>1</div> <div>ENT</div>	Link Parameter CPU KGLWIN
		L	I	N	K		C	O	N	T	R	O	L																					
S	E	L	F	_	S	T	.		N	O	:		h	0	0																			

4.3.7 HS Link Enable

Mode																																			
RUN/PAU		PGM		DEB																															
X				X																															
LOADER					KEY																														
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>7</td><td>.</td><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td></tr> </table>					P	L	C	P	A	R	A	M	E	T	E	R	7	.	H	S	L	I	N	K	E	N	A	B	L	E	<div>ENT</div>				
P	L	C	P	A	R	A	M	E	T	E	R																								
7	.	H	S	L	I	N	K	E	N	A	B	L	E																						
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H	S	L	I	N	K	E	N	A	B	L	E																								
H	S	L	1	E	N	A	B	L	E	:	N	O																							
<table border="1"> <tr><td>H</td><td>S</td><td>L</td><td>I</td><td>N</td><td>K</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td></tr> <tr><td>H</td><td>S</td><td>L</td><td>2</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td>N</td><td>O</td></tr> </table>					H	S	L	I	N	K	E	N	A	B	L	E	H	S	L	2	E	N	A	B	L	E	:	N	O	<div>ON 8 / OFF 8</div> <div>ENT</div>					
H	S	L	I	N	K	E	N	A	B	L	E																								
H	S	L	2	E	N	A	B	L	E	:	N	O																							
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>8</td><td>.</td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td></tr> </table>					P	L	C	P	A	R	A	M	E	T	E	R	8	.	D	E	B	U	G	O	U	T	P	U	T						
P	L	C	P	A	R	A	M	E	T	E	R																								
8	.	D	E	B	U	G	O	U	T	P	U	T																							
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					<div>Link 1 Enable/Disable</div>																														
					<div>Link 2 Enable/Disable</div>																														
					<div>Link Module</div> <div>K200S : 2</div> <div>K300S/1000S : 4</div> <div>K200H/500H/1000H : 1</div>																														

4.3.8 Debug Mode Output

Mode		
RUN/PAU	PGM	DEB
X		X
Debug Mode		
On		MK80S/200S/300S/1000S

LOADER	KEY																										
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr><tr><td>8</td><td>.</td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R	8	.	D	E	B	U	G	O	U	T	P	U	T	<div>ENT</div>	Debug Mode On
P	L	C	P	A	R	A	M	E	T	E	R																
8	.	D	E	B	U	G	O	U	T	P	U	T															
<table border="1"><tr><td></td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td></tr><tr><td></td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td><td>:</td><td>N</td><td>O</td><td></td><td></td></tr></table>		D	E	B	U	G	O	U	T	P	U	T		O	U	T	P	U	T	:	N	O			<div>ON 8</div>		
	D	E	B	U	G	O	U	T	P	U	T																
	O	U	T	P	U	T	:	N	O																		
<table border="1"><tr><td></td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td></tr><tr><td></td><td>O</td><td>U</td><td>T</td><td>P</td><td>U</td><td>T</td><td>:</td><td>Y</td><td>E</td><td>S</td></tr></table>		D	E	B	U	G	O	U	T	P	U	T		O	U	T	P	U	T	:	Y	E	S	<div>OFF 8</div>			
	D	E	B	U	G	O	U	T	P	U	T																
	O	U	T	P	U	T	:	Y	E	S																	
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr><tr><td>9</td><td>.</td><td>I</td><td>N</td><td>T</td><td>E</td><td>R</td><td>R</td><td>U</td><td>P</td><td>T</td><td></td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R	9	.	I	N	T	E	R	R	U	P	T		<div>ENT</div>		
P	L	C	P	A	R	A	M	E	T	E	R																
9	.	I	N	T	E	R	R	U	P	T																	
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4.3.9 Interrupt

1) MK80S/200S/300S/1000S Interrupt

Mode		
RUN/PAU	PGM	DEB
X		X

LOADER	KEY																											
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>9</td><td>.</td><td>I</td><td>N</td><td>T</td><td>E</td><td>R</td><td>R</td><td>U</td><td>P</td><td>T</td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R	9	.	I	N	T	E	R	R	U	P	T		ENT			
P	L	C	P	A	R	A	M	E	T	E	R																	
9	.	I	N	T	E	R	R	U	P	T																		
<table border="1"> <tr><td>#</td><td>0</td><td>0</td><td>1</td><td>.</td><td>T</td><td>D</td><td>I</td><td>2</td><td>.</td><td>I</td><td>N</td><td>T</td></tr> <tr><td>I</td><td>N</td><td>T</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>	#	0	0	1	.	T	D	I	2	.	I	N	T	I	N	T	0	0									1 / 2 ENT	00 Pattern INT / TDINT
#	0	0	1	.	T	D	I	2	.	I	N	T																
I	N	T	0	0																								
<table border="1"> <tr><td>#</td><td>0</td><td>0</td><td>1</td><td>.</td><td>T</td><td>D</td><td>I</td><td>2</td><td>.</td><td>I</td><td>N</td><td>T</td></tr> <tr><td>T</td><td>D</td><td>I</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>*</td><td>1</td><td>0</td><td>m s</td></tr> </table>	#	0	0	1	.	T	D	I	2	.	I	N	T	T	D	I	0	0	0	0	0	0	*	1	0	m s	STEP 2 0 ENT	TDINT 1 200ms
#	0	0	1	.	T	D	I	2	.	I	N	T																
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<table border="1"> <tr><td>#</td><td>0</td><td>1</td><td>1</td><td>.</td><td>T</td><td>D</td><td>I</td><td>2</td><td>.</td><td>I</td><td>N</td><td>T</td></tr> <tr><td>I</td><td>N</td><td>T</td><td>0</td><td>2</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>	#	0	1	1	.	T	D	I	2	.	I	N	T	I	N	T	0	2									2 STEP STEP ENT	01 Pattern INT 02
#	0	1	1	.	T	D	I	2	.	I	N	T																
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<table border="1"> <tr><td>#</td><td>0</td><td>2</td><td>1</td><td>.</td><td>T</td><td>D</td><td>I</td><td>2</td><td>.</td><td>I</td><td>N</td><td>T</td></tr> <tr><td>T</td><td>D</td><td>I</td><td>0</td><td>1</td><td>0</td><td>3</td><td>0</td><td>0</td><td>*</td><td>1</td><td>0</td><td>m s</td></tr> </table>	#	0	2	1	.	T	D	I	2	.	I	N	T	T	D	I	0	1	0	3	0	0	*	1	0	m s	1 STEP 3 0 0 ENT	02 Pattern TDINT 01 ,3
#	0	2	1	.	T	D	I	2	.	I	N	T																
T	D	I	0	1	0	3	0	0	*	1	0	m s																
<table border="1"> <tr><td>#</td><td>0</td><td>7</td><td>1</td><td>.</td><td>T</td><td>D</td><td>I</td><td>2</td><td>.</td><td>I</td><td>N</td><td>T</td></tr> <tr><td>I</td><td>N</td><td>T</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>	#	0	7	1	.	T	D	I	2	.	I	N	T	I	N	T	0	0									STEP STEP STEP STEP STEP	
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#	0	7	1	.	T	D	I	2	.	I	N	T																
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<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>0</td><td>.</td><td>P</td><td>D</td><td>I</td><td>S</td><td>L</td><td>O</td><td>T</td><td>S</td><td>E</td><td>T</td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R	1	0	.	P	D	I	S	L	O	T	S	E	T			
P	L	C	P	A	R	A	M	E	T	E	R																	
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4-17

LOADER	KEY																																																									
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	I	N	T	E	R	R	U	P	T	Y	E	S																																														
	P	O	2	I	N	T	O	O	:	O	N																																															

4.3.10 PDI Slot

Mode																													
RUN/PAU	PGM	DEB																											
X		X																											
Slot			MK200S/500H/1000H MK200S																										
LOADER			KEY																										
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>0</td><td>.</td><td>P</td><td>D</td><td>I</td><td>S</td><td>L</td><td>O</td><td>T</td><td>S</td><td>E</td><td>T</td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R	1	0	.	P	D	I	S	L	O	T	S	E	T	<input type="button" value="ENT"/>	
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<table border="1"> <tr><td></td><td>P</td><td>D</td><td>I</td><td>S</td><td>L</td><td>O</td><td>T</td><td>S</td><td>E</td><td>T</td><td></td></tr> <tr><td></td><td></td><td></td><td>S</td><td>L</td><td>O</td><td>T</td><td>:</td><td>0</td><td>0</td><td></td><td></td></tr> </table>				P	D	I	S	L	O	T	S	E	T					S	L	O	T	:	0	0			<input type="button" value="1"/> ~ <input type="button" value="9"/>		
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<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>1</td><td>.</td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R	1	1	.	F	O	R	C	E	O	N	O	F	F	<input type="button" value="ENT"/>	
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<table border="1"> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>																											1 ~ 11 PDI		

4.3.11 Force On/Off

Mode			
RUN/PAU	PGM	DEB	
X		X	

I/O Parameter

LOADER	KEY																													
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>1</td><td>.</td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R			1	1	.	F	O	R	C	E	O	N	O	F	F		<div>ENT</div>	
P	L	C	P	A	R	A	M	E	T	E	R																			
1	1	.	F	O	R	C	E	O	N	O	F	F																		
<table border="1"> <tr><td></td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td><td></td><td></td></tr> <tr><td>P</td><td>0</td><td>0</td><td>0</td><td>0</td><td>F</td><td>:</td><td>O</td><td>F</td><td>F</td><td>D</td><td>:</td><td>0</td><td></td></tr> </table>		F	O	R	C	E	O	N	O	F	F				P	0	0	0	0	F	:	O	F	F	D	:	0		<div>ON 8 / OFF 8</div> <div>1 / 0</div>	P000 On/Off , Data 1/0
	F	O	R	C	E	O	N	O	F	F																				
P	0	0	0	0	F	:	O	F	F	D	:	0																		
<table border="1"> <tr><td></td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td><td></td><td></td></tr> <tr><td>P</td><td>0</td><td>0</td><td>0</td><td>0</td><td>F</td><td>:</td><td>O</td><td>N</td><td></td><td>D</td><td>:</td><td>1</td><td></td></tr> </table>		F	O	R	C	E	O	N	O	F	F				P	0	0	0	0	F	:	O	N		D	:	1		<div>STEP</div>	P000 On , Data 1
	F	O	R	C	E	O	N	O	F	F																				
P	0	0	0	0	F	:	O	N		D	:	1																		
<table border="1"> <tr><td></td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td><td></td><td></td></tr> <tr><td>P</td><td>0</td><td>0</td><td>0</td><td>1</td><td>F</td><td>:</td><td>O</td><td>F</td><td>F</td><td>D</td><td>:</td><td>0</td><td></td></tr> </table>		F	O	R	C	E	O	N	O	F	F				P	0	0	0	1	F	:	O	F	F	D	:	0		<div>ON 8 / OFF 8</div> <div>1 / 0</div>	P001 On/Off , Data 1/0
	F	O	R	C	E	O	N	O	F	F																				
P	0	0	0	1	F	:	O	F	F	D	:	0																		
<table border="1"> <tr><td></td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td><td></td><td></td></tr> <tr><td>P</td><td>0</td><td>0</td><td>0</td><td>1</td><td>F</td><td>:</td><td>O</td><td>F</td><td>F</td><td>D</td><td>:</td><td>1</td><td></td></tr> </table>		F	O	R	C	E	O	N	O	F	F				P	0	0	0	1	F	:	O	F	F	D	:	1		<div>CLR</div>	P001 Off , Data 1
	F	O	R	C	E	O	N	O	F	F																				
P	0	0	0	1	F	:	O	F	F	D	:	1																		
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>1</td><td>.</td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>O</td><td>N</td><td>O</td><td>F</td><td>F</td><td></td></tr> </table>	P	L	C	P	A	R	A	M	E	T	E	R			1	1	.	F	O	R	C	E	O	N	O	F	F		<div>STEP</div>	I/O
P	L	C	P	A	R	A	M	E	T	E	R																			
1	1	.	F	O	R	C	E	O	N	O	F	F																		
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P	L	C	P	A	R	A	M	E	T	E	R																			
1	2	.	F	O	R	C	E	E	N	A	B	L	E																	
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4.3.12 Force Enable

Mode		
RUN/PAU	PGM	DEB
X		X

Force On/Off Enable .

LOADER	KEY																														
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>1</td><td>2</td><td>.</td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R			1	2	.	F	O	R	C	E	E	N	A	B	L	E	<div>ENT</div> <div>ON 8 / OFF 8</div> <div>ENT</div>	<div>I/O Parameter Enable/Disable .</div>	
P	L	C	P	A	R	A	M	E	T	E	R																				
1	2	.	F	O	R	C	E	E	N	A	B	L	E																		
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<table border="1"><tr><td></td><td></td><td>F</td><td>O</td><td>R</td><td>C</td><td>E</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td></td></tr><tr><td></td><td></td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td><td>:</td><td>Y</td><td>E</td><td>S</td><td></td><td></td></tr></table>			F	O	R	C	E	E	N	A	B	L	E				E	N	A	B	L	E	:	Y	E	S					
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<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>1</td><td>3</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>C</td><td>O</td><td>N</td><td>N</td><td>E</td><td>T</td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R			1	3	.	R	E	M	O	T	E	C	O	N	N	E	T		
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4.3.13 Remote Connection

Mode																																
RUN/PAU	PGM	DEB																														
X		X	MK200S/300S/1000S																													
Remote KLD-150S																																
LOADER			KEY																													
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>1</td><td>3</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>C</td><td>O</td><td>N</td><td>N</td><td>E</td><td>T</td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		1	3	.	R	E	M	O	T	E	C	O	N	N	E	T	<input type="button" value="ENT"/>	
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<table border="1"> <tr><td></td><td></td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>C</td><td>O</td><td>N</td><td>N</td><td>E</td><td>T</td></tr> <tr><td></td><td></td><td>T</td><td>Y</td><td>P</td><td>E</td><td>:</td><td>M</td><td>K</td><td>S</td><td></td><td>F</td><td>n</td><td>e</td><td>t</td></tr> </table>					R	E	M	O	T	E	C	O	N	N	E	T			T	Y	P	E	:	M	K	S		F	n	e	t	<input type="button" value="STEP"/> <input type="button" value="ENT"/>
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		T	Y	P	E	:	M	K	S		F	n	e	t																		
<table border="1"> <tr><td></td><td></td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>C</td><td>O</td><td>N</td><td>N</td><td>E</td><td>T</td></tr> <tr><td></td><td></td><td>S</td><td>L</td><td>O</td><td>T</td><td>:</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>					R	E	M	O	T	E	C	O	N	N	E	T			S	L	O	T	:	0							<input type="button" value="0"/> ~ <input type="button" value="7"/>	
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4.3.14 Remote Disconnection

Mode																																												
RUN/PAU	PGM	DEB																																										
X		X	MK200S/300S/1000S																																									
Remote																																												
LOADER												KEY																																
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>4</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>D</td><td>I</td><td>S</td><td>C</td><td>O</td><td>N</td></tr> </table>												P	L	C	P	A	R	A	M	E	T	E	R	1	4	.	R	E	M	O	T	E	D	I	S	C	O	N	<input type="button" value="ENT"/>		Remote /			
P	L	C	P	A	R	A	M	E	T	E	R																																	
1	4	.	R	E	M	O	T	E	D	I	S	C	O	N																														
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<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>1</td><td>4</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>D</td><td>I</td><td>S</td><td>C</td><td>O</td><td>N</td></tr> </table>												P	L	C	P	A	R	A	M	E	T	E	R	1	4	.	R	E	M	O	T	E	D	I	S	C	O	N	<input type="button" value="STEP"/>					
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1	4	.	R	E	M	O	T	E	D	I	S	C	O	N																														
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4.3.15 Timer

Mode																																
RUN/PAU	PGM	DEB																														
X		X																														
Timer			.(100ms)																													
			100ms Timer 10ms Timer																													
LOADER			KEY																													
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>1</td><td>5</td><td>.</td><td>T</td><td>I</td><td>M</td><td>E</td><td>R</td><td>S</td><td>E</td><td>T</td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		1	5	.	T	I	M	E	R	S	E	T			<input type="button" value="ENT"/>			
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<table border="1"> <tr><td>1</td><td>0</td><td>0</td><td>m</td><td>T</td><td>I</td><td>M</td><td>E</td><td>R</td><td>S</td><td>E</td><td>T</td><td></td></tr> <tr><td></td><td>T</td><td>0</td><td>0</td><td>0</td><td>-</td><td>T</td><td>1</td><td>9</td><td>1</td><td></td><td></td><td></td></tr> </table>			1	0	0	m	T	I	M	E	R	S	E	T			T	0	0	0	-	T	1	9	1				<input type="button" value="1"/> <input type="button" value="0"/> <input type="button" value="0"/>			
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	T	0	0	0	-	T	1	0	0																							
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>1</td><td>6</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		1	6	.	R	E	M	O	T	E	A	C	C	E	S	S		
P	L	C	P	A	R	A	M	E	T	E	R																					
1	6	.	R	E	M	O	T	E	A	C	C	E	S	S																		
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100ms Timer : T000 ~ T100
10ms Timer : T101 ~ T255

4.3.16 Remote Access

Mode		
RUN/PAU	PGM	DEB
X		X

Remote

LOADER	KEY																													
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>1</td><td>6</td><td>.</td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R			1	6	.	R	E	M	O	T	E	A	C	C	E	S	S	<div>ENT</div>
P	L	C	P	A	R	A	M	E	T	E	R																			
1	6	.	R	E	M	O	T	E	A	C	C	E	S	S																
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		R	E	M	O	T	E	A	C	C	E	S	S																	
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<table border="1"><tr><td></td><td></td><td>R</td><td>E</td><td>M</td><td>O</td><td>T</td><td>E</td><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td><td></td></tr><tr><td></td><td></td><td>A</td><td>C</td><td>C</td><td>E</td><td>S</td><td>S</td><td>:</td><td>Y</td><td>E</td><td>S</td><td></td><td></td></tr></table>			R	E	M	O	T	E	A	C	C	E	S	S				A	C	C	E	S	S	:	Y	E	S			<div>ENT</div>
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		A	C	C	E	S	S	:	Y	E	S																			
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Remote

4.3.17 Version Number ()

Mode																																		
RUN/PAU	PGM	DEB																																
X		X																																
			Number	0/S 가 (MK200S/300S/1000S)																														
LOADER			KEY																															
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>7</td><td>.</td><td>V</td><td>E</td><td>R</td><td>S</td><td>I</td><td>O</td><td>N</td><td>N</td><td>O</td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R			1	7	.	V	E	R	S	I	O	N	N	O			ENT			
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<table border="1"> <tr><td></td><td></td><td></td><td>S</td><td>L</td><td>O</td><td>T</td><td>:</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>						S	L	O	T	:	0	0																			STEP ▼	Slot 00 I/O		
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			S	L	O	T	:	0	1																									
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			S	L	O	T	:	0	1																									
F	n	e	t	9	8	0	3	V	1	.	5																							
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1	8	.	C	O	M	M	U	N	I	C	A	T	I	O	N																			
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4.3.18 Communication Parameter

Mode			
RUN/PAU	PGM	DEB	
X		X	MK200S

LOADER	KEY	
PLC PARAMETER 18. COMMUNICATION	[ENT]	
< PLC STATION > NUMBER : 00	[0] ~ [9]	.(0 ~ 31)
< BAUD RATE > 1 : 9600 bps	[STEP] [STEP] [STEP] [ENT]	Baudrate (9600, 19200, 38400, 56000, 57600, 76800, 115200, 128000)
< MASTER / SLAVE > 1 : SLAVE SET	[STEP] [ENT]	Master/Slave
< MASTER / SLAVE > 2 : MASTER SET	[ENT]	Master
< MASTER TIMEOUT > 00200 * 10ms	[0] ~ [9] [ENT]	Timeout (0 ~ 65535)
< SLAVE READ ENB > READ ENABLE : NO	[ON 8] / [OFF 8]	Slave Read 가
PLC PARAMETER 19. COMM. ENABLE	[ENT]	

4.3.19 Communication Enable

Mode																															
RUN/PAU	PGM	DEB																													
X		X	MK80S																												
			/																												
LOADER		KEY																													
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>1</td><td>9</td><td>.</td><td>C</td><td>O</td><td>M</td><td>M</td><td>.</td><td>E</td><td>N</td><td>A</td><td>B</td><td>L</td><td>E</td></tr></table>		P	L	C	P	A	R	A	M	E	T	E	R			1	9	.	C	O	M	M	.	E	N	A	B	L	E	<div>ENT</div>	/
P	L	C	P	A	R	A	M	E	T	E	R																				
1	9	.	C	O	M	M	.	E	N	A	B	L	E																		
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2	.	D	I	S	A	B	L	E																							
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2	0	.	P	I	D																										
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4.3.20 PID

Mode																															
RUN/PAU	PGM	DEB																													
X		X			MK80S																										
PID																															
LOADER			KEY																												
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>2</td><td>0</td><td>.</td><td>P</td><td>I</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R	2	0	.	P	I	D							<div>ENT</div>				
P	L	C	P	A	R	A	M	E	T	E	R																				
2	0	.	P	I	D																										
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1	.	C	A	L	C	U	L	A	T	I	O	N																			
2	.	A	U	T	O	T	U	N	I	N	G																				
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			L	O	O	P	#	0	0																						
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			L	O	O	P	#	0	0																						
		M	V	_	M	A	N	0	0	0	0																				
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			L	O	O	P	#	0	0																						
		M	V	_	M	A	N	0	0	0	0																				
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			L	O	O	P	#	0	0																						
		M	V	_	M	A	X	0	0	0	0																				
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						N	0	0	0	0	0																				
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			L	O	O	P	#	0	0																						
						T	T	0	0	0	0																				
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P	L	C	P	A	R	A	M	E	T	E	R																				
2	0	.	P	I	D																										
			K80S																												

4.3.21 Pulse Catch

Mode					
RUN/PAU	PGM	DEB			
X		X			MK80S
LOADER			KEY		
PLC PARAMETER 22. PULSE CATCH			<input type="button" value="ENT"/>		
P0000 : ENABLE : DISABLE			<input type="button" value="STEP"/>		
			<input type="button" value="ENT"/>		
P0001 : ENABLE : DISABLE			<input type="button" value="STEP"/>		
			<input type="button" value="ENT"/>		
P0002 : ENABLE : DISABLE			<input type="button" value="STEP"/>		
			<input type="button" value="ENT"/> <input type="button" value="ENT"/> <input type="button" value="ENT"/>		
P0005 : ENABLE : DISABLE			<input type="button" value="STEP"/>		
			<input type="button" value="ENT"/> <input type="button" value="ENT"/>		
P0007 : ENABLE : DISABLE			<input type="button" value="STEP"/>		
			<input type="button" value="ENT"/>		
PLC PARAMETER 23. INPUT FILTER					
<div></div>					
<div></div>					
			CPU		
			MK80S		

4.3.22 Input Filter

<div>Mode</div> <table border="1"><tr><td>RUN/PAU</td><td>PGM</td><td>DEB</td></tr><tr><td>X</td><td></td><td>X</td></tr></table>		RUN/PAU	PGM	DEB	X		X																															
RUN/PAU	PGM	DEB																																				
X		X																																				
					MK80S																																	
LOADER					KEY																																	
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>2</td><td>3</td><td>.</td><td>I</td><td>N</td><td>P</td><td>U</td><td>T</td><td>F</td><td>I</td><td>L</td><td>T</td><td>E</td><td>R</td></tr></table>					P	L	C	P	A	R	A	M	E	T	E	R			2	3	.	I	N	P	U	T	F	I	L	T	E	R	<div>ENT</div>					
P	L	C	P	A	R	A	M	E	T	E	R																											
2	3	.	I	N	P	U	T	F	I	L	T	E	R																									
<table border="1"><tr><td>F</td><td>I</td><td>L</td><td>T</td><td>E</td><td>R</td><td>I</td><td>N</td><td>G</td><td>T</td><td>I</td><td>M</td><td>E</td><td></td></tr><tr><td></td><td></td><td></td><td></td><td>0</td><td>8</td><td>*</td><td></td><td>1</td><td>m</td><td>s</td><td></td><td></td><td></td></tr></table>					F	I	L	T	E	R	I	N	G	T	I	M	E						0	8	*		1	m	s				<div>0</div> ~ <div>9</div> <div>ENT</div>		(0 ~ 15)			
F	I	L	T	E	R	I	N	G	T	I	M	E																										
				0	8	*		1	m	s																												
<table border="1"><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr><tr><td>2</td><td>4</td><td>.</td><td>A</td><td>N</td><td>A</td><td>L</td><td>O</td><td>G</td><td>U</td><td>E</td><td>P</td><td>R</td><td>M</td></tr></table>					P	L	C	P	A	R	A	M	E	T	E	R			2	4	.	A	N	A	L	O	G	U	E	P	R	M						
P	L	C	P	A	R	A	M	E	T	E	R																											
2	4	.	A	N	A	L	O	G	U	E	P	R	M																									
<table border="1"><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>																																						
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					CPU																																	
					MK80S																																	

4.3.23 Analog Parameter

Mode																														
RUN/PAU	PGM	DEB																												
X		X	MK80S																											
LOADER		KEY																												
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td></tr> <tr><td>2</td><td>4</td><td>.</td><td>A</td><td>N</td><td>A</td><td>L</td><td>O</td><td>G</td><td>U</td><td>E</td><td>P</td><td>R</td><td>M</td><td>.</td></tr> </table>		P	L	C	P	A	R	A	M	E	T	E	R	2	4	.	A	N	A	L	O	G	U	E	P	R	M	.	ENT	
P	L	C	P	A	R	A	M	E	T	E	R																			
2	4	.	A	N	A	L	O	G	U	E	P	R	M	.																
<table border="1"> <tr><td>U</td><td>N</td><td>I</td><td>T</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>A</td><td>/</td><td>D</td><td>C</td><td>H</td><td>1</td><td>V</td><td>_</td><td>I</td><td></td><td></td><td></td></tr> </table>		U	N	I	T	1								A	/	D	C	H	1	V	_	I				ENT	1 A/D 1			
U	N	I	T	1																										
A	/	D	C	H	1	V	_	I																						
<table border="1"> <tr><td>1</td><td>.</td><td>C</td><td>U</td><td>R</td><td>R</td><td>E</td><td>N</td><td>T</td><td></td><td></td><td></td></tr> <tr><td>2</td><td>.</td><td>V</td><td>O</td><td>L</td><td>T</td><td>A</td><td>G</td><td>E</td><td></td><td></td><td></td></tr> </table>		1	.	C	U	R	R	E	N	T				2	.	V	O	L	T	A	G	E				STEP ENT	/			
1	.	C	U	R	R	E	N	T																						
2	.	V	O	L	T	A	G	E																						
<table border="1"> <tr><td>A</td><td>/</td><td>D</td><td>C</td><td>H</td><td>1</td><td>I</td><td>K</td><td>I</td><td>N</td><td>D</td></tr> <tr><td>0</td><td>2</td><td>0</td><td>m</td><td>A</td><td></td><td>4</td><td>2</td><td>0</td><td>m</td><td>A</td></tr> </table>		A	/	D	C	H	1	I	K	I	N	D	0	2	0	m	A		4	2	0	m	A	STEP ENT	0 ~ 20 / 4 ~ 20mA					
A	/	D	C	H	1	I	K	I	N	D																				
0	2	0	m	A		4	2	0	m	A																				
<table border="1"> <tr><td>U</td><td>N</td><td>I</td><td>T</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>A</td><td>/</td><td>D</td><td>C</td><td>H</td><td>2</td><td>V</td><td>_</td><td>I</td><td></td><td></td></tr> </table>		U	N	I	T	1							A	/	D	C	H	2	V	_	I			ENT	2					
U	N	I	T	1																										
A	/	D	C	H	2	V	_	I																						
<table border="1"> <tr><td>1</td><td>.</td><td>C</td><td>U</td><td>R</td><td>R</td><td>E</td><td>N</td><td>T</td><td></td><td></td><td></td></tr> <tr><td>2</td><td>.</td><td>V</td><td>O</td><td>L</td><td>T</td><td>A</td><td>G</td><td>E</td><td></td><td></td><td></td></tr> </table>		1	.	C	U	R	R	E	N	T				2	.	V	O	L	T	A	G	E				STEP ENT	2			
1	.	C	U	R	R	E	N	T																						
2	.	V	O	L	T	A	G	E																						
<table border="1"> <tr><td>U</td><td>N</td><td>I</td><td>T</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>D</td><td>/</td><td>A</td><td>V</td><td>_</td><td>I</td><td></td><td></td><td></td><td></td><td></td></tr> </table>		U	N	I	T	1							D	/	A	V	_	I							D/A (A/D .)					
U	N	I	T	1																										
D	/	A	V	_	I																									
<table border="1"> <tr><td>U</td><td>N</td><td>I</td><td>T</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>D</td><td>/</td><td>A</td><td>S</td><td>T</td><td>O</td><td>P</td><td>C</td><td>L</td><td>E</td><td>A</td><td>R</td></tr> </table>		U	N	I	T	1							D	/	A	S	T	O	P	C	L	E	A	R	ENT					
U	N	I	T	1																										
D	/	A	S	T	O	P	C	L	E	A	R																			
<table border="1"> <tr><td>1</td><td>.</td><td>C</td><td>L</td><td>E</td><td>A</td><td>R</td><td></td><td></td><td></td><td></td></tr> <tr><td>2</td><td>.</td><td>D</td><td>O</td><td>N</td><td>O</td><td>T</td><td>C</td><td>L</td><td>E</td><td>A</td><td>R</td></tr> </table>		1	.	C	L	E	A	R					2	.	D	O	N	O	T	C	L	E	A	R		1 Error / 2 MK80S				
1	.	C	L	E	A	R																								
2	.	D	O	N	O	T	C	L	E	A	R																			
			CPU																											

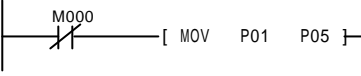
4.4

4.4.1 Step

Mode			
RUN/PAU	PGM	DEB	
		x	
		'PGM'	'RUN'
LOADER		KEY	
<div>0 1 2 3 4</div> <div>L O A D N O T M 0 0 1 0</div>		SHFT CLR	가
<div>0 0 0 0 0</div> <div></div>		1 3 5 7	SHFT CLR Key '0000'
<div>0 1 3 5 7</div> <div></div>		STEP	
<div>0 1 3 5 7</div> <div>A N D M 0 0 0 1</div>		STEP	'PGM' 'RUN'
<div>0 1 3 5 6</div> <div>L O A D N O T M 0 0 0 0</div>		STEP STEP	On/Off 가
<div>0 1 3 5 8</div> <div>O U T P 0 0 0 0</div>		CLR	STEP STEP Key Up/Down
<div>0 1 3 5 8</div> <div>O U T</div>		CLR	'P0000'
<div>0 1 3 5 8</div> <div></div>		CLR	'OUT'
<div>0 0 0 0 0</div> <div></div>			0

4.4.2

Mode		
RUN/PAU	PGM	DEB
×		×



LOADER	KEY																																								
<table border="1" style="width: 100%;"> <tr> <td></td><td>K</td><td>2</td><td>0</td><td>0</td><td>S</td><td>A</td><td></td><td></td><td>V</td><td>X</td><td>.</td><td>X</td> </tr> <tr> <td>#</td><td>#</td><td>P</td><td>R</td><td>O</td><td>G</td><td>R</td><td>A</td><td>M</td><td></td><td>#</td><td>#</td><td></td> </tr> </table>		K	2	0	0	S	A			V	X	.	X	#	#	P	R	O	G	R	A	M		#	#		<div style="border: 1px solid black; padding: 2px; display: inline-block;">CLR</div>	PGM 가 "X.X" O/S Version No.													
	K	2	0	0	S	A			V	X	.	X																													
#	#	P	R	O	G	R	A	M		#	#																														
<table border="1" style="width: 100%;"> <tr> <td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> </table>	0	0	0	0	0									<div style="border: 1px solid black; padding: 2px; display: inline-block;">LOAD</div> <div style="border: 1px solid black; padding: 2px; display: inline-block;">NOT</div> <div style="border: 1px solid black; padding: 2px; display: inline-block;">M</div> <div style="border: 1px solid black; padding: 2px; display: inline-block;">0</div>	<div style="border: 1px solid black; padding: 2px; display: inline-block;">CLR</div>																										
0	0	0	0	0																																					
<table border="1" style="width: 100%;"> <tr> <td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>O</td><td>O</td> </tr> <tr> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> </table>	0	0	0	0	0									L	O	A	D		N	O	T			M	O	O														<div style="border: 1px solid black; padding: 2px; display: inline-block;">ENT</div>	
0	0	0	0	0																																					
L	O	A	D		N	O	T			M	O	O																													
<table border="1" style="width: 100%;"> <tr> <td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td>F</td><td>U</td><td>N</td><td>(</td><td>0</td><td>0</td><td>0</td><td>)</td><td></td><td></td><td></td><td>N</td><td>O</td> </tr> <tr> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td></td> </tr> </table>	0	0	0	0	1									F	U	N	(0	0	0)				N	O												P		<div style="border: 1px solid black; padding: 2px; display: inline-block;">FUN</div> <div style="border: 1px solid black; padding: 2px; display: inline-block;">8</div> <div style="border: 1px solid black; padding: 2px; display: inline-block;">0</div>	0 가 <div style="border: 1px solid black; padding: 2px; display: inline-block;">ENT</div> Key
0	0	0	0	1																																					
F	U	N	(0	0	0)				N	O																													
											P																														
<table border="1" style="width: 100%;"> <tr> <td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td> </tr> <tr> <td>F</td><td>U</td><td>N</td><td>(</td><td>0</td><td>8</td><td>0</td><td>)</td><td></td><td></td><td></td><td>M</td><td>O</td> </tr> <tr> <td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>V</td><td></td> </tr> </table>	0	0	0	0	1									F	U	N	(0	8	0)				M	O												V		<div style="border: 1px solid black; padding: 2px; display: inline-block;">ENT</div>	'80' 가 'MOV' 가 가 <div style="border: 1px solid black; padding: 2px; display: inline-block;">ENT</div> Key
0	0	0	0	1																																					
F	U	N	(0	8	0)				M	O																													
											V																														
		Function <div style="border: 1px solid black; padding: 2px; display: inline-block;">ENT</div> Key																																							

1) PGM, STEP, STEP, SRCH, Key, Word No., Byte No., Data.

4.4.3

Mode																																													
RUN/PAU	PGM	DEB																																											
x		x																																											
, n 'NOP'			1																																										
LOADER			KEY																																										
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>0</td><td>U</td><td>T</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	1																0	U	T								P	0	0	0	0						OUT M 0 1 0		INST Key
0	0	0	0	1																																									
0	U	T								P	0	0	0	0																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>0</td><td>R</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>1</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	1																0	R									M	0	0	1	0						INST		'1' M010
0	0	0	0	1																																									
0	R									M	0	0	1	0																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>2</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>0</td><td>U</td><td>T</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	2																0	U	T								P	0	0	0	0						ENT		'3' OUT P0000 2 1 가
0	0	0	0	2																																									
0	U	T								P	0	0	0	0																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>3</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	3																L	O	A	D							P	0	0	0	1						LOAD P 0 2 0		'3' OUT P10 '4' 1 가
0	0	0	0	3																																									
L	O	A	D							P	0	0	0	1																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>3</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>2</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	3																L	O	A	D							P	0	0	2	0						INST FUN 8 0		P020
0	0	0	0	3																																									
L	O	A	D							P	0	0	2	0																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>4</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>F</td><td>U</td><td>N</td><td>(</td><td>0</td><td>8</td><td>0</td><td>)</td><td></td><td></td><td>M</td><td>0</td><td>V</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	4																F	U	N	(0	8	0)			M	0	V								INST		'4' [MOV P05 P03]
0	0	0	0	4																																									
F	U	N	(0	8	0)			M	0	V																																	
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td><</td><td>1</td><td>></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	5																<	1	>								P	0	0	5							P 0 5 INST		
0	0	0	0	5																																									
<	1	>								P	0	0	5																																

LOADER	KEY	
<div>0 0 0 0 7</div> <div>< 2 ></div> <div>MOV</div> <div>P 0 0 3</div>	<div>P 0 3</div> <div>INST</div>	<div>'9 10 NOP</div> <div>9 ~ 18 NOP가</div>
<div>0 0 0 0 9</div> <div>LOAD</div> <div>P 0 0 0 1</div>	<div>CLR CLR</div> <div>SHIFT</div>	<div>< N ></div>
<div>0 0 0 0 9</div> <div>SF</div>	<div>1 0</div>	
<div>0 0 0 0 9</div> <div>0 0 0 1 0</div>	<div>INST</div>	<div>9 ~ 18 NOP</div>
<div>0 0 0 0 9</div> <div>F U N (0 0 0)</div> <div>N O P</div>		

1) INST Key ENT Key	
2)	
3) Device ENT Key INST Key	
4) INST Key Buzzer 2 INST Key 1 1	
<div>0 0 0 0 1</div> <div>OR</div> <div>M 0 0 1 0</div>	

4.4.4

Mode					
RUN/PAU	PGM	DEB			

1 ~ n

0000 M0000 (OUT P0000)

0002 P001 (TON 00100 T0000)

0006 T000 M010 (OUT P0010)

0000 M0000 (OUT P0000)

0002 T0000 (OUT P0000)

LOADER	KEY	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 2 L O A D P 0 0 0 1 </div>	<div style="border: 1px solid black; padding: 2px;"> DELT </div>	<p>P0001</p> <p>Timer, Counter Word No. Word Data</p> <p>'3 ~ 5 [TON 00100 T0000]</p> <p>'7 M0010</p> <p>N 10 (OUT P0010)</p> <p>'3 ~ 12 13 가</p>
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 2 T O N T 0 0 0 0 </div>	<div style="border: 1px solid black; padding: 2px;"> DELT </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 2 L O A D T 0 0 0 0 </div>	<div style="border: 1px solid black; padding: 2px;"> STEP or ENT </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 3 A N D N O T M 0 0 1 0 </div>	<div style="border: 1px solid black; padding: 2px;"> DELT </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 3 O U T P 0 0 1 0 </div>	<div style="border: 1px solid black; padding: 2px;"> CLR CLR </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 3 L O A D SF </div>	<div style="border: 1px solid black; padding: 2px;"> SHIFT </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 3 0 0 0 1 0 </div>	<div style="border: 1px solid black; padding: 2px;"> 0 0 1 0 </div>	
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 3 L O A D M 0 0 2 0 </div>	<div style="border: 1px solid black; padding: 2px;"> DELT </div>	

1) Timer, Counter, Word No., Word Data가 가 ()	2) DELT Key 가 2 (DELT Key 1 .)	'd' 가 Buzzer 1 ,
<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 6 M I D D . E R R < 2 > P 0 0 0 </div>	<div style="border: 1px solid black; padding: 2px;"> 0 0 0 0 2 d T O N T 0 0 0 0 </div>	

Mode		
RUN/PAU	PGM	DEB
x		x

Timer, Counter

1)

0	0	0	0	0															
L	O	A	D											P	0	0	0	0	

0	0	0	0	1															
T	O	N												T	0	0	0	0	

0	0	0	0	2															
<	D	A	T	A	>														

0	0	0	0	2															
<	D	A	T	A	>									0	0	1	0	0	

0	0	0	0	4															
L	O	A	D											T	0	0	0	0	

0	0	0	0	5															
O	U	T												P	0	0	0	2	

LOAD P 0 ENT

TMR T 0

ENT

1 0 0

ENT

LOAD T 0

ENT

OUT P 2

ENT

TON P0000

TMR Key , Timer

TMR Key 1 TON,

2 TOFF, 3 TMR,

4 TMON, 5 TRTG,

6 TON

Timer

Timer

[TON T0000 00100]

'LOAD T0000'

T0000

'OUT P0002'

[OUT P0002]

LOADER	KEY	
2)		
0 0 0 0 0 L O A D	LOAD P 0 ENT	
	LOAD P 5 ENT	
0 0 0 0 1 L O A D	CNT C 0	0 Step
	ENT	CNT Key ,
0 0 0 0 2 C T U		CNT Key 1 CTU,
		2 CTD, 3 CTUD,
		4 CTR, 5
		CTU가
0 0 0 0 3 D A T A	1 0 0	
	ENT	
0 0 0 0 3 D A T A	LOAD C 0	
		'LOAD C0000'
0 0 0 0 5 L O A D	ENT	
	OUT P 3	
		'OUT P0003'
0 0 0 0 6 O U T		

1)	PGM	STEP STEP PGM	Key
2)	TMR	CNT	Timer, Counter가
	ENT		

1) Timer/Counter 가 'ENT' Key 'OPRND.ERR' 가

) Timer / Counter 65535

(Device Hex,Decimal Error .)

0	0	0	0	2	0	P	R	N	D	.	E	R	R	
D	A	T	A								6	5	3	6

2) FUN 가 130 ~ 147 BCD "B" Key

3) 'D'가 (: DMOV) Double Word 'P'가

(: MOVP) Pulse

4) TMR Key (PGM)) 5) CNT Key (PGM))

ENT Key ENT Key

0	0	0	0	1										
T	O	N								T	O	O	O	O

On Delay Timer TMR

0	0	0	0	1										
T	O	F	F							T	O	O	O	O

Off Delay Timer TMR

0	0	0	0	1										
T	M	R								T	O	O	O	O

Accumulation Timer TMR

0	0	0	0	1										
T	M	O	N							T	O	O	O	O

Monostable Timer TMR

0	0	0	0	1										
T	R	T	G							T	O	O	O	O

Retriggerable Monostable Timer TMR

0	0	0	0	1										
C	T	U								C	O	O	O	O

Up Counter CNT

0	0	0	0	1										
C	T	D								C	O	O	O	O

Down Counter CNT

0	0	0	0	1										
C	T	U	D							C	O	O	O	O

Up - Down Counter CNT

0	0	0	0	1										
C	T	R								C	O	O	O	O

Ring Counter CNT

6) ENT Key 'e' 가 가

ENT Key 1 1 \

0	0	0	0	3	e									
D	A	T	A							0	0	4	3	2

4.5 (Search)

4.5.1

Mode				
RUN/PAU	PGM	DEB		
		×		
			, Timer, Counter	
			SRCH Key	
LOADER			KEY	
0 0 0 0 4 P 0 0 0 0			P 0 0 0 SRCH	
0 0 0 0 4 P 0 0 0 0			SRCH	
0 0 0 2 1 0 U T P 0 0 0 0			SRCH	
0 0 0 5 0 0 U T P 0 0 0 0			SRCH	
L O A D 0 U T P 0 0 0 0			SRCH	
0 0 2 2 0 A N D P 0 0 0 0			SRCH	
0 0 2 2 1 O R P 0 0 0 0			SRCH	


<div></div>	
1) Search 0 0 0 0 4 S e a r c h i n g P 0 0 0 0	2) Search Bit No.가 0 0 0 0 4 N o t F o u n d P 0 0 0 0

4.5.2 (Word)


Mode					
RUN/PAU	PGM	DEB			
		x			CLR Key Clear
					END SRCH
LOADER			KEY		
0 0 0 5 0 cd			CLR CLR A		Word No. cd Word No.
0 0 0 5 0 cd P 0 0 3			P 0 3 SRCH		Word No.
0 0 1 2 3 < 1 > D E C P P 0 0 3			CLR CLR FUN 0 8 0		SRCH Key P03 Word No.가
0 0 1 2 3 F U N (0 8 0) M O V			SRCH		(MOV)
0 0 1 5 7 F U N (0 8 0) M O V			STEP		SRCH Key 가
0 0 1 5 8 < 1 > M O V 0 0 0 4 5			SRCH		Word No.가 Decimal
0 0 1 7 2 < 1 > A D D 0 0 0 4 5			CLR CLR FUN 0 0 1		SRCH Key '045' Decimal
0 0 1 7 2 F U N (0 0 1) E N D			SRCH		'END' 가
0 1 5 2 0 F U N (0 0 1) E N D					SRCH Key 'END' 가

4.6

4.6.1

Mode																																										
RUN/PAU	PGM	DEB																																								
	×	×																																								
RUN																																										
LOADER		KEY																																								
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>	0	0	0	0	0																L	O	A	D		N	O	T			M	0	0	0	0						<div>STEP STEP</div>	<div>SHFT CLR Key '0000'</div>
0	0	0	0	0																																						
L	O	A	D		N	O	T			M	0	0	0	0																												
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>2</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>1</td><td></td><td></td><td></td><td></td><td></td></tr> </table>	0	0	0	0	2																L	O	A	D		N	O	T			M	0	0	0	1						<div>STEP</div>	<div>' ' 'LOAD NOT M0001' 가</div> <div>가</div>
0	0	0	0	2																																						
L	O	A	D		N	O	T			M	0	0	0	1																												
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>3</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>A</td><td>N</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>1</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>	0	0	0	0	3																A	N	D								M	0	0	1	0						<div>STEP</div>	<div>'3' Device</div>
0	0	0	0	3																																						
A	N	D								M	0	0	1	0																												
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>4</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>O</td><td>U</td><td>T</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td><td>2</td><td></td><td></td><td></td><td></td><td></td></tr> </table>	0	0	0	0	4																O	U	T								P	0	0	0	2						<div>STEP</div>	<div>'3' 1 가 '4'</div>
0	0	0	0	4																																						
O	U	T								P	0	0	0	2																												
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>F</td><td>U</td><td>N</td><td>(</td><td>0</td><td>0</td><td>1</td><td>)</td><td></td><td></td><td></td><td></td><td></td><td></td><td>E</td><td>N</td><td>D</td><td></td><td></td><td></td></tr> </table>	0	0	0	0	5																F	U	N	(0	0	1)							E	N	D					<div>'4' 1 가 '5'</div>
0	0	0	0	5																																						
F	U	N	(0	0	1)							E	N	D																										

4.6.2

Mode					
RUN/PAU	PGM	DEB			
	×	×			
4					
LOADER			KEY		
<div>0 0 0 0 2</div> <div>L O A D N O T M 0 0 0 1</div>			<div>PGM</div> <div>RUN</div>		
<div>0 0 0 0 2</div> <div>L O A D N O T M 0 0 0 1</div>			<div>CLR CLR</div>		
<div>0 0 0 0 2</div> <div></div>			<div>M 0 0 0 0</div> <div>MON</div>		
<div></div> <div></div>			<div>P 0 0 0 0</div> <div>MON</div>		
<div></div> <div>M 0 0 0 0 P 0 0 0 0</div>			<div>M 0 0 0 1</div> <div>MON</div>		
<div></div> <div></div>			<div>M 0 0 1 0</div> <div>MON</div>		
<div></div> <div>P 0 0 0 0 M 0 0 0 1</div>			<div>P 0 0 0 2</div> <div>MON</div>		
<div></div> <div>M 0 0 0 0 P 0 0 0 0</div>			<div>M 0 0 1 0</div> <div>MON</div>		
<div></div> <div>M 0 0 0 1 M 0 0 1 0</div>			<div>CLR CLR CLR CLR</div>		
<div></div> <div>P 0 0 0 0 M 0 0 0 1</div>			<div>STEP</div>		
<div></div> <div>M 0 0 1 0 P 0 0 0 2</div>					
<div>0 0 0 0 2</div> <div></div>					
<div>0 0 0 0 2</div> <div>L O A D N O T M 0 0 0 1</div>					

4.6.3

Mode						
RUN/PAU	PGM	DEB				
	×	×				
Word No. Bit, Decimal, Hexa . Decimal, Hexa 3 Word No. 가 .						
LOADER			KEY			
<div>0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</div> <div>L O A D N O T M O O O O</div>			<div>RUN</div> <div>CLR CLR</div>		RUN Word No.	
<div>0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</div>			<div>↵A P 0</div>			
<div>0 0 0 0 3 0 0 0 0 0 0 0 0 0 0 0</div> <div>↵ P 0 0 0</div>			<div>MON</div>		'P0'	
<div>0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</div> <div>h 0 0 6 4 P 0 0 0</div>			<div>P 3 MON</div>		'P0' (Decimal Key) 'P0'	
<div>0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</div> <div>h 0 0 6 4 P 0 0 3 h 0 0 B E</div>			<div>P 5 MON</div>		P000, P003	
<div>0 0 0 0 0 0 0 0 0 0 0 0 0 0 0 0</div> <div>h 0 0 B E P 0 0 3 h 0 0 5 h 0 0 6 5</div>			<div>SHFT MON</div>		2 3	

LOADER	KEY																													
<table><tr><td>F</td><td>E</td><td>D</td><td>C</td><td>B</td><td>A</td><td>9</td><td>8</td><td>7</td><td></td><td>P</td><td>0</td><td>0</td><td>5</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	F	E	D	C	B	A	9	8	7		P	0	0	5															<div>STEP</div>	P005 (K200S, K300S, K1000S 16bit .)
F	E	D	C	B	A	9	8	7		P	0	0	5																	
<table><tr><td>F</td><td>E</td><td>D</td><td>C</td><td>B</td><td>A</td><td>9</td><td>8</td><td>7</td><td></td><td>P</td><td>0</td><td>0</td><td>4</td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	F	E	D	C	B	A	9	8	7		P	0	0	4															<div>CLR</div>	<div>STEP</div> Key P004 Word
F	E	D	C	B	A	9	8	7		P	0	0	4																	
<table><tr><td></td><td></td><td>P</td><td>0</td><td>0</td><td>3</td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>4</td></tr><tr><td></td><td></td><td>h</td><td>0</td><td>0</td><td>B</td><td>E</td><td></td><td></td><td>h</td><td>1</td><td>1</td><td>7</td><td>2</td></tr></table>			P	0	0	3				P	0	0	4			h	0	0	B	E			h	1	1	7	2	<div>P</div> <div>5</div> <div>MON</div>	P004, P005 Word No. P003 Word shift	
		P	0	0	3				P	0	0	4																		
		h	0	0	B	E			h	1	1	7	2																	
<table><tr><td></td><td></td><td>P</td><td>0</td><td>0</td><td>4</td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>5</td></tr><tr><td></td><td></td><td>h</td><td>1</td><td>1</td><td>7</td><td>2</td><td></td><td></td><td>h</td><td>0</td><td>0</td><td>6</td><td>5</td></tr></table>			P	0	0	4				P	0	0	5			h	1	1	7	2			h	0	0	6	5	<div>CLR</div>		
		P	0	0	4				P	0	0	5																		
		h	1	1	7	2			h	0	0	6	5																	
<table><tr><td></td><td></td><td>P</td><td>0</td><td>0</td><td>3</td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>4</td></tr><tr><td></td><td></td><td>h</td><td>0</td><td>0</td><td>B</td><td>E</td><td></td><td></td><td>h</td><td>1</td><td>1</td><td>7</td><td>2</td></tr></table>			P	0	0	3				P	0	0	4			h	0	0	B	E			h	1	1	7	2	<div>MON</div>	P005 P003, P004 Word No. .	
		P	0	0	3				P	0	0	4																		
		h	0	0	B	E			h	1	1	7	2																	
<table><tr><td></td><td></td><td>P</td><td>0</td><td>0</td><td>4</td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td></td><td>h</td><td>1</td><td>1</td><td>7</td><td>2</td><td></td><td></td><td>h</td><td>0</td><td>0</td><td>6</td><td>4</td></tr></table>			P	0	0	4				P	0	0	0			h	1	1	7	2			h	0	0	6	4	<div>MON</div>	<div>MON</div> Key shift P000 4 Word가 .	
		P	0	0	4				P	0	0	0																		
		h	1	1	7	2			h	0	0	6	4																	

1)	2 Word가 가	4 Word	.
<div>MON</div> 2 Key	가 <div>CLR</div> Key	Word가	shift 가
2) New Master - K	3 Word	가	.

4.6.4 /

Mode						
RUN/PAU	PGM	DEB				
	×	×				
Timer, Counter						
LOADER			KEY			
<div>0 0 0 0 0 0</div> <div>T O N</div> <div>0 0 0 0 0</div>			<div>MON</div>		Timer, Counter	
<div>0 0 0 0 0 0</div> <div>O N T 0 0 0 0</div> <div>0 0 1 0 0</div>			<div>C 0</div> <div>MON</div>		On Delay Timer T0000	
<div>0 N T 0 0 0 0</div> <div>U P C 0 0 0 0</div> <div>0 0 1 0 0</div>			<div>STEP</div>		Up Counter C0000	
<div>0 N T 0 0 0 0</div> <div>N U C 0 0 0 1</div> <div>0 0 1 0 0</div>			<div>STEP</div>		Device	
<div>0 N T 0 0 0 0</div> <div>U P C 0 0 0 0</div> <div>0 0 1 0 0</div>			<div>STEP</div>		Key	
<div>0 N T 0 0 0 0</div> <div>U P C 0 0 0 0</div> <div>0 0 1 0 0</div>			<div>STEP</div>		Device	
<div>0 N T 0 0 0 0</div> <div>U P C 0 0 0 0</div> <div>0 0 1 0 0</div>			<div>CLR</div>		(Device)	
<div>0 0 0 0 0</div> <div>< S ></div> <div>< P ></div>			<div>Device 가</div>			
<div>0 N</div> <div>0 0 1 0 0</div> <div>0 0 1 0 0</div>			<div>SHIFT</div> <div>MON</div>			
<div>0 0 0 0 0</div> <div>< S ></div> <div>< P ></div>			<div>* B</div>			
<div>0 N</div> <div>h 0 0 6 4</div> <div>h 0 0 6 4</div>			<div>CLR</div>		Hexa, Decimal	
<div>0 N T 0 0 0 0</div> <div>h 0 0 6 4</div>			<div>CLR</div>		Key	

1) Timer / Counter

'D

'D Word No.' 가

4.6.5 I/O Table

Mode																														
RUN/PAU	PGM	DEB																												
	×																													
PLC I/O . (K10, K30S, K60S, K200S, K300S, K1000S)																														
LOADER			KEY																											
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>1</td><td>.</td><td>L</td><td>A</td><td>T</td><td>C</td><td>H</td><td>A</td><td>R</td><td>E</td><td>A</td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		1	.	L	A	T	C	H	A	R	E	A			PRM STEP STEP STEP ▼ ▼ ▼	
P	L	C	P	A	R	A	M	E	T	E	R																			
1	.	L	A	T	C	H	A	R	E	A																				
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>4</td><td>.</td><td>I</td><td>/</td><td>O</td><td>T</td><td>A</td><td>B</td><td>L</td><td>E</td><td></td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		4	.	I	/	O	T	A	B	L	E				MON STEP STEP STEP ▼ ▼ ▼	
P	L	C	P	A	R	A	M	E	T	E	R																			
4	.	I	/	O	T	A	B	L	E																					
<table border="1"> <tr><td></td><td>I</td><td>/</td><td>O</td><td>T</td><td>A</td><td>B</td><td>L</td><td>E</td><td>M</td><td>O</td><td>N</td><td></td></tr> <tr><td>S</td><td>T</td><td>0</td><td>0</td><td>:</td><td>D</td><td>C</td><td>I</td><td>3</td><td>2</td><td></td><td></td><td></td></tr> </table>				I	/	O	T	A	B	L	E	M	O	N		S	T	0	0	:	D	C	I	3	2				STEP STEP STEP ▼ ▼ ▼ STEP STEP STEP ▼ ▼ ▼	
	I	/	O	T	A	B	L	E	M	O	N																			
S	T	0	0	:	D	C	I	3	2																					
<table border="1"> <tr><td></td><td>I</td><td>/</td><td>O</td><td>T</td><td>A</td><td>B</td><td>L</td><td>E</td><td>M</td><td>O</td><td>N</td><td></td></tr> <tr><td>S</td><td>T</td><td>0</td><td>6</td><td>:</td><td>T</td><td>R</td><td>O</td><td>3</td><td>2</td><td></td><td></td><td></td></tr> </table>				I	/	O	T	A	B	L	E	M	O	N		S	T	0	6	:	T	R	O	3	2				STEP STEP STEP ▼ ▼ ▼ STEP STEP STEP ▼ ▼ ▼	
	I	/	O	T	A	B	L	E	M	O	N																			
S	T	0	6	:	T	R	O	3	2																					
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr> <tr><td>5</td><td>.</td><td>P</td><td>A</td><td>S</td><td>S</td><td>W</td><td>O</td><td>R</td><td>D</td><td></td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R		5	.	P	A	S	S	W	O	R	D				I/O Table I/O K200S : I/O 12 가 K300S : I/O 32 가 K1000S : I/O 32 가 6 Slot TR 32 I/O	
P	L	C	P	A	R	A	M	E	T	E	R																			
5	.	P	A	S	S	W	O	R	D																					

4.7 Test

4.7.1 On/Off

Mode																																													
RUN/PAU	PGM	DEB																																											
	×	×																																											
/ Word No.																																													
LOADER			KEY																																										
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>0</td><td>A</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	0																L	0	A	D							M	0	0	0	0						<div>CLR</div> <div>CLR</div>		
0	0	0	0	0																																									
L	0	A	D							M	0	0	0	0																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	0																																				<div>M</div> <div>0</div> <div>MON</div>		
0	0	0	0	0																																									
<table border="1"> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>																																	M	0	0	0	0						<div>8</div>		
										M	0	0	0	0																															
<table border="1"> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>																																	M	0	0	0	0						<div>P</div> <div>0</div> <div>MON</div>		
										M	0	0	0	0																															
<table border="1"> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td>P</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>																							M	0	0	0	0						P	0	0	0	0						<div>CLR</div> <div>CLR</div> <div>A</div> <div>M</div> <div>0</div> <div>MON</div>		
M	0	0	0	0						P	0	0	0	0																															

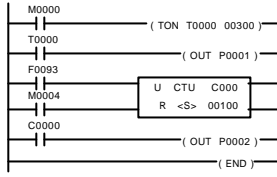
- 1) 가 .
- 2) 'F' On/Off .

4.7.2

Mode				
RUN/PAU	PGM	DEB		
	×	×		
Word No.		Word No.		
<div> <div>0 0 0 0 0</div> <div>L O A D</div> </div> <div> <div>0 0 0 0 0</div> </div> <div> <div>P 0 0 1</div> <div>h 0 0 3 7</div> </div> <div> <div>P 0 0 1</div> <div>? ? ? ? ? T</div> </div> <div> <div>P 0 0 1</div> <div>h 0 0 A 9</div> </div> <div> <div>F E D C B A 9 8 7</div> <div>P 0 0 1</div> </div> <div> <div>F E D C B A 9 8 7</div> <div>M 0 0 0</div> </div> <div> <div>F E D C B A 9 8 7</div> <div>M 0 0 0</div> </div> <div> <div>P 0 0 1</div> <div>h 3 0 A 9</div> </div>		<div> <div>CLR</div> <div>CLR</div> </div> <div> <div>↺A</div> <div>P</div> <div>1</div> <div>MON</div> </div> <div> <div>TEST</div> </div> <div> <div>↺B</div> <div>A</div> <div>9</div> <div>ENT</div> </div> <div> <div>SHFT</div> <div>MON</div> </div> <div> <div>TEST</div> </div> <div> <div>STEP</div> <div>STEP</div> <div>↺8</div> <div>↺8</div> <div>ENT</div> </div> <div> <div>CLR</div> </div>		<div> <div>F0093</div> <div>(DECP P0001)</div> <div>(END)</div> </div> <div>Step Monitor</div> <div>Key</div> <div> P001 Word 가 A9H K10S, K10S1, K60H, K100S, K200H, K250 K500H, K1000H, K200S, K300S, K1000S 16bit </div> <div> M000 14 , 13 bit On </div> <div> <div>↺8</div> <div>↺9</div> <div>Key</div> </div> <div> M000 14 , 13 bit On </div> <div> <div>CLR</div> <div>Key</div> <div>Word</div> </div>

4-53

4.7.4 / On/Off

Mode						
RUN/PAU	PGM	DEB				
			Timer, Counter		On/Off	
						
LOADER			KEY			
<div>0 0 0 0 0</div>			<div>CLR CLR</div>			
<div>0 0 0 0 0</div>			<div>T 0 MON</div>			
<div>0 N T 0 0 0 0</div>			<div>TEST</div>		T0000 Timer	
<div>0 0 3 0 0</div>					'300'	
<div>0 N T 0 0 0 0</div>			<div>1 0 0</div>		On	
<div>* ? ? ? ? ? T</div>						
<div>0 N T 0 0 0 0</div>			<div>ENT</div>		'100'	
<div>* 0 0 1 0 0 T</div>						
<div>0 N T 0 0 0 0</div>			<div>arg</div>			
<div>0 0 1 0 0</div>						
<div>0 N T 0 0 0 0</div>			<div>C 0 MON</div>		Off '100'	
<div>0 0 1 0 0</div>					가 '300'	

LOADER	KEY																													
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>3</td><td>0</td><td>0</td><td></td><td></td><td>0</td><td>0</td><td>3</td><td>3</td><td>9</td><td></td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	3	0	0			0	0	3	3	9		<div>TEST</div>	C000
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	3	0	0			0	0	3	3	9																		
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td></td><td>*</td><td>?</td><td>?</td><td>?</td><td>?</td><td>?</td><td>T</td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	1	0	0		*	?	?	?	?	?	T	<div>5</div> <div>0</div>	Device
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	1	0	0		*	?	?	?	?	?	T																	
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td></td><td>*</td><td></td><td>0</td><td>0</td><td>0</td><td>5</td><td>T</td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	1	0	0		*		0	0	0	5	T	<div>ENT</div>	
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	1	0	0		*		0	0	0	5	T																	
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td></td><td></td><td></td><td>0</td><td>0</td><td>0</td><td>5</td><td>1</td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	1	0	0				0	0	0	5	1	<div>off 9</div>	
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	1	0	0				0	0	0	5	1																	
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td></td><td></td><td></td><td>0</td><td>0</td><td>0</td><td>7</td><td>2</td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	1	0	0				0	0	0	7	2		C0000 Off
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	1	0	0				0	0	0	7	2																	
<table><tr><td>O</td><td>N</td><td>T</td><td>0</td><td>0</td><td>0</td><td>0</td><td>U</td><td>P</td><td>C</td><td>0</td><td>0</td><td>0</td><td>0</td></tr><tr><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>0</td><td></td><td></td><td></td><td>0</td><td>0</td><td>1</td><td>0</td><td>4</td></tr></table>	O	N	T	0	0	0	0	U	P	C	0	0	0	0		0	0	1	0	0				0	0	1	0	4		C0000가 On Counter 가.
O	N	T	0	0	0	0	U	P	C	0	0	0	0																	
	0	0	1	0	0				0	0	1	0	4																	

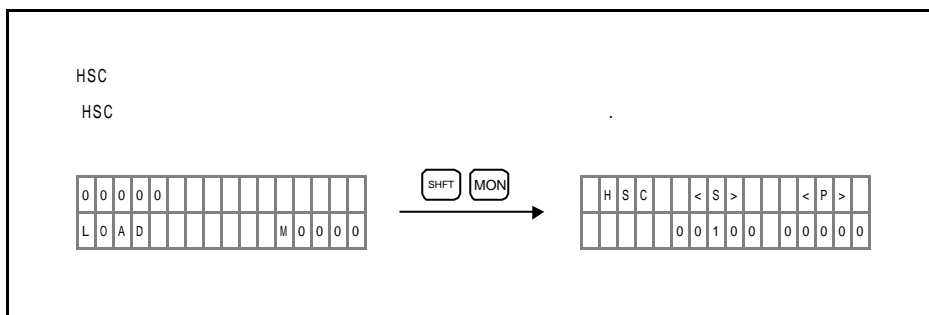
1) K200S, K300S, K1000S Timer, Counter On/Off가 .

4.8 HSC()

4.8.1 Data

Mode																															
RUN/PAU	PGM	DEB																													
x		x																													
High Speed Counter			K10S/K10S1, K30S, K60S																												
LOADER			KEY																												
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>2</td><td>.</td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R			2	.	H	S	C	S	E	T							PRM STEP ENT
P	L	C	P	A	R	A	M	E	T	E	R																				
2	.	H	S	C	S	E	T																								
<table border="1"> <tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td><td></td><td></td></tr> <tr><td><</td><td>D</td><td>A</td><td>T</td><td>A</td><td>></td><td></td><td></td><td></td><td></td><td>0</td><td>0</td><td>0</td><td>0</td></tr> </table>					H	S	C	S	E	T			0	0			<	D	A	T	A	>					0	0	0	0	1 0 0
		H	S	C	S	E	T			0	0																				
<	D	A	T	A	>					0	0	0	0																		
<table border="1"> <tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>#</td><td>0</td><td>0</td><td></td></tr> <tr><td><</td><td>D</td><td>A</td><td>T</td><td>A</td><td>></td><td></td><td></td><td></td><td></td><td>0</td><td>0</td><td>1</td><td>0</td></tr> </table>					H	S	C	S	E	T			#	0	0		<	D	A	T	A	>					0	0	1	0	ENT
		H	S	C	S	E	T			#	0	0																			
<	D	A	T	A	>					0	0	1	0																		
<table border="1"> <tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td><td></td><td></td></tr> <tr><td><</td><td>S</td><td>E</td><td>T</td><td>></td><td>=</td><td></td><td></td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr> </table>					H	S	C	S	E	T			0	0			<	S	E	T	>	=			0	0	0	0	0	0	1 0 1 0
		H	S	C	S	E	T			0	0																				
<	S	E	T	>	=			0	0	0	0	0	0																		
<table border="1"> <tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td><td></td><td></td></tr> <tr><td><</td><td>S</td><td>E</td><td>T</td><td>></td><td>=</td><td></td><td></td><td>1</td><td>0</td><td>1</td><td>0</td><td>*</td><td>*</td></tr> </table>					H	S	C	S	E	T			0	0			<	S	E	T	>	=			1	0	1	0	*	*	1 0 0 0
		H	S	C	S	E	T			0	0																				
<	S	E	T	>	=			1	0	1	0	*	*																		
<table border="1"> <tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td><td></td><td></td></tr> <tr><td><</td><td>S</td><td>E</td><td>T</td><td>></td><td>=</td><td></td><td></td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td></tr> </table>					H	S	C	S	E	T			0	0			<	S	E	T	>	=			1	0	1	0	1	0	ENT
		H	S	C	S	E	T			0	0																				
<	S	E	T	>	=			1	0	1	0	1	0																		

LOADER	KEY																															
<table><tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td></tr><tr><td><</td><td>R</td><td>S</td><td>T</td><td>></td><td>=</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td></tr></table>			H	S	C		S	E	T			0	0	<	R	S	T	>	=	0	0	0	0	0	0	0	<table><tr><td>1</td><td>0</td><td>1</td><td>0</td></tr></table>	1	0	1	0	P01 Card Off
		H	S	C		S	E	T			0	0																				
<	R	S	T	>	=	0	0	0	0	0	0	0																				
1	0	1	0																													
<table><tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td></tr><tr><td><</td><td>R</td><td>S</td><td>T</td><td>></td><td>=</td><td>1</td><td>0</td><td>1</td><td>0</td><td>*</td><td>*</td><td>*</td></tr></table>			H	S	C		S	E	T			0	0	<	R	S	T	>	=	1	0	1	0	*	*	*	<table><tr><td>1</td><td>0</td><td>0</td><td>0</td></tr></table>	1	0	0	0	P01 Card Off
		H	S	C		S	E	T			0	0																				
<	R	S	T	>	=	1	0	1	0	*	*	*																				
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<table><tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>0</td></tr><tr><td><</td><td>R</td><td>S</td><td>T</td><td>></td><td>=</td><td>1</td><td>0</td><td>1</td><td>0</td><td>1</td><td>0</td><td>0</td></tr></table>			H	S	C		S	E	T			0	0	<	R	S	T	>	=	1	0	1	0	1	0	0	<table><tr><td>ENT</td></tr></table>	ENT				
		H	S	C		S	E	T			0	0																				
<	R	S	T	>	=	1	0	1	0	1	0	0																				
ENT																																
<table><tr><td></td><td></td><td>H</td><td>S</td><td>C</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td><td>0</td><td>1</td></tr><tr><td><</td><td>D</td><td>A</td><td>T</td><td>A</td><td>></td><td></td><td></td><td></td><td></td><td>0</td><td>0</td><td>0</td></tr></table>			H	S	C		S	E	T			0	1	<	D	A	T	A	>					0	0	0	<table><tr><td>CLR</td></tr></table>	CLR	HSC 0 ~ 19 가			
		H	S	C		S	E	T			0	1																				
<	D	A	T	A	>					0	0	0																				
CLR																																
<table><tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr><tr><td>2</td><td>.</td><td>H</td><td>S</td><td>C</td><td>S</td><td>E</td><td>T</td><td></td><td></td><td></td><td></td><td></td></tr></table>	P	L	C	P	A	R	A	M	E	T	E	R		2	.	H	S	C	S	E	T							Parameter				
P	L	C	P	A	R	A	M	E	T	E	R																					
2	.	H	S	C	S	E	T																									



4.8.2

Mode					
RUN/PAU	PGM	DEB			
		×			
High Speed Counter Clear			HSC Setting → Default Parameter		
LOADER		KEY			
<div>0 0 0 0 1</div> <div>LOAD NOT MOOD 0</div>		<div>PRM STEP</div> <div>▼</div>			
<div>P L C P A R A M E T E R</div> <div>2 . H S C S E T</div>		<div>SHIFT DELT</div>			
<div>* D E F A U L T *</div> <div>* P R M W R I T E ? *</div>		<div>ENT</div>		('HSC Set' Clear)	
<div>* D E F A U L T *</div> <div>* C O M P L E T E D ! *</div>		<div>CLR</div>			
<div>P L C P A R A M E T E R</div> <div>2 . H S C S E T</div>					
<div>0 0 0 0 0</div> <div></div>		<div>STEP</div> <div>▼</div>			
<div>0 0 0 0 0</div> <div>LOAD NOT MOOD 0</div>					

4.9 (Debug)

4.9.1 1Step

Mode																																													
RUN/PAU	PGM	DEB																																											
X	X																																												
			1																																										
LOADER			KEY																																										
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>O</td><td>O</td><td>O</td><td>O</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	0																L	O	A	D		N	O	T			M	O	O	O	O						<div>PGM</div> <div>DEB PAU</div>		
0	0	0	0	0																																									
L	O	A	D		N	O	T			M	O	O	O	O																															
<table border="1"> <tr><td>#</td><td>#</td><td></td><td></td><td></td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td></td><td>#</td><td>#</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td>M</td><td>O</td><td>D</td><td>E</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			#	#				D	E	B	U	G		#	#													M	O	D	E												<div>CLR</div>		
#	#				D	E	B	U	G		#	#																																	
					M	O	D	E																																					
<table border="1"> <tr><td>1</td><td>.</td><td>T</td><td>R</td><td>A</td><td>C</td><td>E</td><td></td><td>2</td><td>.</td><td>S</td><td>T</td><td>E</td><td>P</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>3</td><td>.</td><td>S</td><td>C</td><td>A</td><td>N</td><td></td><td></td><td>4</td><td>.</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			1	.	T	R	A	C	E		2	.	S	T	E	P							3	.	S	C	A	N			4	.	V	A	L	U	E						<div>1</div>		
1	.	T	R	A	C	E		2	.	S	T	E	P																																
3	.	S	C	A	N			4	.	V	A	L	U	E																															
<table border="1"> <tr><td></td><td></td><td>S</td><td>T</td><td>A</td><td>R</td><td>T</td><td></td><td>S</td><td>T</td><td>E</td><td>P</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>1</td><td>.</td><td>F</td><td>I</td><td>R</td><td>S</td><td>T</td><td></td><td>2</td><td>.</td><td>B</td><td>.</td><td>P</td><td>.</td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>					S	T	A	R	T		S	T	E	P									1	.	F	I	R	S	T		2	.	B	.	P	.							<div>1</div>		
		S	T	A	R	T		S	T	E	P																																		
1	.	F	I	R	S	T		2	.	B	.	P	.																																
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	0																																				<div>STEP</div>		
0	0	0	0	0																																									
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>O</td><td>O</td><td>O</td><td>O</td><td></td><td></td><td></td><td></td><td></td></tr> </table>			0	0	0	0	0																L	O	A	D		N	O	T			M	O	O	O	O						<div>STEP</div>		
0	0	0	0	0																																									
L	O	A	D		N	O	T			M	O	O	O	O																															
			<p>Trace(1 Step Run)</p> <p>Step</p> <p>-- First : 1Step</p> <p>-- B.P : Step</p> <p>1 Step RUN '00000' Step</p>																																										

LOADER	KEY	
<div>0 0 0 0 1</div> <div>A N D M O O 2</div>	STEP ▼	
<div>0 0 0 0 1</div> <div>A N D N O T M O O 4</div>	STEP ▼	
<div>0 0 0 0 3</div> <div>O U T P O O 0</div>	STEP ▼	
<div>0 0 0 0 4</div> <div>L O A D N O T P O O 0</div>	STEP ▼	
<div>0 0 0 0 5</div> <div>F U N (0 8 0) M O V</div>	STEP ▼	1 RUN
<div>0 0 0 1 0</div> <div>F U N (0 0 1) E N D</div>	CLR CLR	
<div>1 . T R A C E 2 . S T E P</div> <div>3 . S C A N 4 . V A L U E</div>		

4.9.2 Step

Mode					
RUN/PAU	PGM	DEB			
×	×				
Break Point					
LOADER			KEY		
0 0 0 0 0 L O A D N O T M 0 0 0 0			PGM DEB PAU		
# # D E B U G # # M O D E			CLR		
1 . T R A C E 2 . S T E P 3 . S C A N 4 . V A L U E			2		
\$ S T E P B R E A K B R E A K S T E P = ? ? ? ?			5		
\$ S T E P B R E A K B R E A K S T E P = 0 0 0 5			ENT		
S T A R T S T E P 1 . F I R S T 2 . B . P .			1 STEP		
0 0 0 0 0 R u n n i n g					
0 0 0 0 5 F U N (0 8 0) M O V			CLR CLR		
\$ S T E P B R E A K B R E A K S T E P = ? ? ? ? ?			CLR		
1 . T R A C E 2 . S T E P 3 . S C A N 4 . V A L U E					
			Debug Debug Function 1. TRACE : 2. STEP : 3. SCAN : 4. VALUE : 5 Break point Break Run 2 B.P. B.P. 5 Break Break point Debug		

4.9.3 Scan

Mode					
RUN/PAU	PGM	DEB			
×	×				<pre> 0000 M0000 M0002 M0004 (OUT P0000) P0000 0004 P0000 (MOV 55 P0001) M0005 0010 P0005 (DECP P0002) 0014 (END) </pre>
Scan					
LOADER		KEY			
0 0 0 0 0 L O A D N O T M 0 0 0 0		PGM DEB PAU		Debug	
# # D E B U G # # M O D E		CLR		Debug Function	
1 . T R A C E 2 . S T E P 3 . S C A N 4 . V A L U E		3		1. TRACE : 2. STEP : 3. SCAN : 4. VALUE :	
\$ S C A N B R E A K B R E A K S C A N = ? ? ? ?		2 0 0		Break point Scan	
\$ S C A N B R E A K B R E A K S C A N = 0 2 0 0		ENT		65535 Scan	
S T A R T S T E P 1 . F I R S T 2 . B . P .		1 STEP		Break Run	
0 0 0 0 0 R u n n i n g				2 B.P. B.P.	
0 0 0 0 5 F U N (0 0 1) E N D		CLR CLR		200 Scan END	
\$ S C A N B R E A K B R E A K S C A N = ? ? ? ?		CLR		Break point	
1 . T R A C E 2 . S T E P 3 . S C A N 4 . V A L U E				Debug	

4.9.4

Mode																																				
RUN/PAU	PGM	DEB																																		
×	×																																			
Card Break Point																																				
LOADER			KEY																																	
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>O</td><td>O</td><td>O</td><td>O</td><td></td></tr> </table>			0	0	0	0													L	O	A	D		N	O	T			M	O	O	O	O		<div>PGM</div> <div>DEB PAU</div>	
0	0	0	0																																	
L	O	A	D		N	O	T			M	O	O	O	O																						
<table border="1"> <tr><td>#</td><td>#</td><td></td><td></td><td>D</td><td>E</td><td>B</td><td>U</td><td>G</td><td></td><td>#</td><td>#</td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td>M</td><td>O</td><td>D</td><td>E</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			#	#			D	E	B	U	G		#	#									M	O	D	E									<div>CLR</div>	Debug
#	#			D	E	B	U	G		#	#																									
				M	O	D	E																													
<table border="1"> <tr><td>1</td><td>.</td><td>T</td><td>R</td><td>A</td><td>C</td><td>E</td><td></td><td>2</td><td>.</td><td>S</td><td>T</td><td>E</td><td>P</td><td></td><td></td></tr> <tr><td>3</td><td>.</td><td>S</td><td>C</td><td>A</td><td>N</td><td></td><td></td><td>4</td><td>.</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td></td></tr> </table>			1	.	T	R	A	C	E		2	.	S	T	E	P			3	.	S	C	A	N			4	.	V	A	L	U	E		<div>4</div>	Debug
1	.	T	R	A	C	E		2	.	S	T	E	P																							
3	.	S	C	A	N			4	.	V	A	L	U	E																						
<table border="1"> <tr><td>\$</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td></td><td>B</td><td>R</td><td>E</td><td>A</td><td>K</td><td></td><td></td><td></td><td></td></tr> <tr><td>O</td><td>P</td><td>:</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			\$	V	A	L	U	E		B	R	E	A	K					O	P	:														<div>↵A</div> <div>P</div> <div>2</div> <div>ENT</div>	Break point Device
\$	V	A	L	U	E		B	R	E	A	K																									
O	P	:																																		
<table border="1"> <tr><td>\$</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td></td><td>B</td><td>R</td><td>E</td><td>A</td><td>K</td><td></td><td></td><td></td><td></td></tr> <tr><td>O</td><td>P</td><td>:</td><td>↵</td><td>P</td><td>0</td><td>0</td><td>2</td><td>=</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>			\$	V	A	L	U	E		B	R	E	A	K					O	P	:	↵	P	0	0	2	=								<div>"B</div> <div>↵9</div> <div>6</div>	,Card 가 가
\$	V	A	L	U	E		B	R	E	A	K																									
O	P	:	↵	P	0	0	2	=																												

LOADER	KEY																														
<table><tr><td>\$</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td>B</td><td>R</td><td>E</td><td>A</td><td>K</td><td></td><td></td></tr><tr><td>0</td><td>P</td><td>:</td><td></td><td>P</td><td>0</td><td>0</td><td>2</td><td>=</td><td>h</td><td>0</td><td>0</td><td>9</td><td>6</td></tr></table>	\$	V	A	L	U	E	B	R	E	A	K			0	P	:		P	0	0	2	=	h	0	0	9	6	<div>ENT</div>	<div>P02 Card Data h96 Break stop</div>		
\$	V	A	L	U	E	B	R	E	A	K																					
0	P	:		P	0	0	2	=	h	0	0	9	6																		
<table><tr><td></td><td></td><td>S</td><td>T</td><td>A</td><td>R</td><td>T</td><td>S</td><td>T</td><td>E</td><td>P</td><td></td><td></td></tr><tr><td>1</td><td>.</td><td>F</td><td>I</td><td>R</td><td>S</td><td>T</td><td>2</td><td>.</td><td>B</td><td>.</td><td>P</td><td>.</td></tr></table>			S	T	A	R	T	S	T	E	P			1	.	F	I	R	S	T	2	.	B	.	P	.	<div>1</div>	<div>Break Run</div> <div>2 B.P. B.P.</div>			
		S	T	A	R	T	S	T	E	P																					
1	.	F	I	R	S	T	2	.	B	.	P	.																			
<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	0	0	0	0	0																								<div>STEP</div>		
0	0	0	0	0																											
<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td>0</td><td>0</td><td>2</td><td>4</td><td>9</td><td></td></tr><tr><td></td><td></td><td>R</td><td>u</td><td>n</td><td>n</td><td>i</td><td>n</td><td>g</td><td></td><td>h</td><td>0</td><td>0</td><td>F</td><td>9</td></tr></table>	0	0	0	0	0				0	0	2	4	9				R	u	n	n	i	n	g		h	0	0	F	9		
0	0	0	0	0				0	0	2	4	9																			
		R	u	n	n	i	n	g		h	0	0	F	9																	
<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td>↑</td><td></td><td></td></tr><tr><td>F</td><td>U</td><td>N</td><td>(</td><td>0</td><td>2</td><td>5</td><td>)</td><td></td><td></td><td></td><td>D</td><td>E</td><td>C</td><td>P</td></tr></table>	0	0	0	0	5							↑			F	U	N	(0	2	5)				D	E	C	P	<div>CLR</div> <div>CLR</div>	<div>P02 word h96 ↑ Pulse</div>
0	0	0	0	5							↑																				
F	U	N	(0	2	5)				D	E	C	P																	
<table><tr><td>\$</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td><td>B</td><td>R</td><td>E</td><td>A</td><td>K</td><td></td><td></td></tr><tr><td>0</td><td>P</td><td>:</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>	\$	V	A	L	U	E	B	R	E	A	K			0	P	:											<div>CLR</div>	<div>Value Break Run Device</div>			
\$	V	A	L	U	E	B	R	E	A	K																					
0	P	:																													
<table><tr><td>1</td><td>.</td><td>T</td><td>R</td><td>A</td><td>C</td><td>E</td><td>2</td><td>.</td><td>S</td><td>T</td><td>E</td><td>P</td><td></td></tr><tr><td>3</td><td>.</td><td>S</td><td>C</td><td>A</td><td>N</td><td></td><td>4</td><td>.</td><td>V</td><td>A</td><td>L</td><td>U</td><td>E</td></tr></table>	1	.	T	R	A	C	E	2	.	S	T	E	P		3	.	S	C	A	N		4	.	V	A	L	U	E		<div>Debug</div>	
1	.	T	R	A	C	E	2	.	S	T	E	P																			
3	.	S	C	A	N		4	.	V	A	L	U	E																		

4.10 (Auxiliary)

4.10.1 Step Clear

Mode		
RUN/PAU	PGM	DEB
x		x

0 1 2 2 5

L O A D F 0 0 9 3

1 . M C L R 2 . D C L R

3 . S U B S . 4 . B L O C K

S T A R T S T E P :

S T A R T S T E P : 0 0 1 0 0

E N D S T E P :

S T A R T S T E P : 0 0 1 0 0

E N D S T E P : 0 0 2 5 0

0 0 1 0 0 . 0 0 2 5 0

* M E M O R Y C L E A R ? *

* M E M O R Y *

* * C l e a r e d ! * *

1 . M C L R 2 . D C L R

3 . S U B S . 4 . B L O C K

AUX

1

1 0 0 ENT

2 5 0

ENT

ENT

CLR

PGM AUX Key

Memory Clear

Clear 가

Memory Clear Step

Clear

```

graph LR
    S0000 --- M0000 --- O0003["(OUT P0003)"]
    S1225 --- F0093 --- O1225["(INCP P0005)"]
  
```

4.10.2 Device Clear

Mode				
RUN/PAU	PGM	DEB		
x		x		
LOADER			KEY	
0 0 0 0 1 T O N			PGM AUX Key	
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K			2	
* D A T A C L E A R * P M K L S T C D			P	
* D A T A C L E A R * M K L S T C D			M	
* D A T A C L E A R * K L S T C D			K	
* D A T A C L E A R * L S T C D			S	
* D A T A C L E A R * L T C D			D	
* D A T A C L E A R * L T C			ENT	
L T C * D A T A C L E A R ? *			ENT	
* * D A T A * * * C l e a r e d ! *			CLR	
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K			Clear	

4.10.3 Device (Step)

Mode						
RUN/PAU	PGM	DEB				
x		x				
			Device	Device		
LOADER			KEY			
0 0 0 0 0 L O A D M 0 0 0 0 0			PGM AUX		PGM (AUX) Key	
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K			3		Clear 가	
F R O M :			M ENT		Device	
F R O M : M 0 0 0 0 0 T O :			M 1 2 3		M0000 Device	
F R O M : M 0 0 0 0 0 T O : M 0 1 2 3			ENT		M0000 M0123	
S T A R T S T E P :			0 ENT			
S T A R T S T E P : 0 0 0 0 0 E N D S T E P :			4			
S T A R T S T E P : 0 0 0 0 0 E N D S T E P : 0 0 0 0 4			ENT		0 ~ 4	
* 0 0 0 0 S T E P * * S u b s t i t u t i n g ! *						
* 0 0 0 2 S T E P * * S u b s t i t u t e d ! *			CLR		. (2 가)	
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K					Clear	

4.10.4 Block Move/Copy

Mode					
RUN/PAU	PGM	DEB			
x		x			
			Block	Copy	Move
LOADER			KEY		
0 0 0 0 0 L O A D M 0 0 0 0			PGM AUX Key Clear 가		
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K			PGM AUX 4		
1 . B L O C K M O V E 2 . B L O C K C O P Y			1 2 Block Move Block Copy		
S T A R T S T E P : S T A R T S T E P : 0 0 0 2 0 E N D S T E P :			2 0 ENT Block 3 0 Block		
S T A R T S T E P : 0 0 0 2 0 E N D S T E P : 0 0 0 3 0			ENT 20 30		
0 0 0 2 0 0 0 0 3 0 D E S T . S T E P :			6 0 20 30 60		
0 0 0 2 0 0 0 0 3 0 D E S T . S T E P : 0 0 0 6 0			ENT . (Block Move) . (Block Copy)		
* B L O C K M O V E * * * C o m p l e t e d ! * *			CLR (Block Move) (Block copy)		
* B L O C K C O P Y * * * C o m p l e t e d ! * *			CLR (Block copy) Clear		
1 . M C L R 2 . D C L R 3 . S U B S . 4 . B L O C K					

Error가



4.11 EPROM

4.11.1 Check

Mode					
RUN/PAU	PGM	DEB			
x		x			EPROM 가 가
KLD-150S Handy Loader Blank Check			EPROM		
LOADER			KEY		
0 0 0 0 0 L O A D M O O 0 0 0			PGM CLR EPRM		
1 . E P R O M W R I T E R 2 . F L A S H			1		EPROM Flash Memory (2.FLASH K200S)
1 . E . C H K 2 . W R I T E 3 . V E R I F Y 4 . R E A D			1		EPROM Function 가
* C H E C K E P R O M * 1 2 8 K R O M < E N T >			STEP		EPROM
* C H E C K E P R O M * 5 1 2 K R O M < E N T >			ENT		
* E P R O M E . C H K * * * 0 1 3 8 0 S T E P * *					Erase Check (ROM Blank Check)
* E P R O M E . C H K * * * C O M P L E T E D ! *			CLR		EPROM Function
1 . E . C H K 2 . W R I T E 3 . V E R I F Y 4 . R E A D					

1) EPROM	2) CLR Key
* E P R O M E . C H K * * * F A I L E D ! * *	* E P R O M E . C H K * * * C a n c e l e d ! * *
3) 가 EPROM EPROM Write 가 Vpp=12.5V , Vcc=6V 가 128K ROM : TM 27C128(T.I), D27128A(INTEL) 256K ROM : MBM27C256A-25(FUJITSU), NMC27C256(NEC) RAM Back up 가 DS1230(DALLAS)	

4.11.2 Write

Mode					
RUN/PAU	PGM	DEB			
x		x			
KLD-150S Handy Loader PLC			EPROM, RAM Pack		<div style="display: flex; align-items: center; justify-content: center;"> <div style="text-align: center;"> PLC CPU () </div> <div style="margin: 0 10px;"> Write → </div> <div style="text-align: center;"> KLD-150S ROM Pack or RAM Pack </div> </div>
LOADER			KEY		
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 0 0 0 0 0 </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> L O A D M 0 0 0 0 </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">PGM</div> <div style="border: 1px solid black; padding: 2px;">CLR</div> <div style="border: 1px solid black; padding: 2px;">EPRM</div> </div>		
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E P R O M W R I T E R </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 2 . F L A S H </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">1</div> </div>		EPROM Flash Memory (2. FLASH K200S .)
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E C H K 2 . W R I T E </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 3 . V E R I F Y 4 . R E A D </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">2</div> </div>		EPROM Function
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * C H E C K E P R O M * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 2 8 K R O M < E N T > </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">STEP</div> × 2 </div>		EPROM, RAM
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * C H E C K E P R O M * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 2 5 6 K R A M < E N T > </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">ENT</div> </div>		
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * E P R O M W R I T E * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * * 0 3 6 8 S T E P * * </div>					PLC EPROM, RAM Pack
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * E P R O M W R I T E * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * * C O M P L E T E D ! * * </div>			<div style="display: flex; justify-content: space-around;"> <div style="border: 1px solid black; padding: 2px;">CLR</div> </div>		
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E . C H K 2 . W R I T E </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 3 . V E R I F Y 4 . R E A D </div>					EPROM Function

1) EPROM Write Error가

* E P R O M W R I T E *

* * F A I L E D ! * *

4.11.3 Verify

Mode										KLD-150S Handy Loader										KLD-150S ROM Pack or RAM Pack									
RUN/PAU					PGM					DEB					PLC CPU					Verify					KLD-150S ROM Pack or RAM Pack				
x										x					()									
KLD-150S Handy Loader										EPROM, RAM Pack																			
LOADER										KEY																			
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 0 0 0 0 0 </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> L O A D M 0 0 0 0 </div>										<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-around;"> PGM CLR EPRM </div>																			
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E P R O M W R I T E R </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 2 . F L A S H </div>										<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: center; align-items: center;"> 1 </div>										EPROM Flash Memory (2. FLASH K200S									
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E C H K 2 . W R I T E </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 3 . V E R I F Y 4 . R E A D </div>										<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: center; align-items: center;"> 2 </div>										EPROM Function									
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * C H E C K E P R O M * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 2 8 K R O M < E N T > </div>										<div style="border: 1px solid black; padding: 2px; display: flex; align-items: center;"> <div style="border: 1px solid black; padding: 2px; text-align: center;">STEP ▼</div> × 3 </div>										EPROM, RAM									
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * C H E C K E P R O M * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 2 5 6 K R O M < E N T > </div>										<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: center; align-items: center;"> ENT </div>																			
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * E P R O M V E R I F Y * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * * 0 5 6 7 S T E P * * </div>																				EPROM, RAM Pack CPU									
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * E P R O M V E R I F Y * </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> * * C O M P L E T E D ! * * </div>										<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: center; align-items: center;"> CLR </div>																			
<div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 1 . E . C H K 2 . W R I T E </div> <div style="border: 1px solid black; padding: 2px; display: flex; justify-content: space-between;"> 3 . V E R I F Y 4 . R E A D </div>																				EPROM Function									

4.11.4 Read

Mode					
RUN/PAU	PGM	DEB			
x		x			
KLD-150S Handy Loader PLC			EPROM, RAM Pack		PLC CPU ()
					Read KLD-150S ROM Pack or RAM Pack
LOADER			KEY		
0 0 0 0 0 L O A D M 0 0 0 0			PGM CLR EPRM		
1 . E P R O M W R I T E R 2 . F L A S H			1		EPROM Flash Memory (2. FLASH K200S .)
1 . E C H K 2 . W R I T E 3 . V E R I F Y 4 . R E A D			2		EPROM Function
* C H E C K E P R O M * 1 2 8 K R O M < E N T >			STEP x 3		EPROM, RAM
* C H E C K E P R O M * 2 5 6 K R O M < E N T >			ENT		
* E P R O M R E A D * * * 0 2 7 8 S T E P * *					EPROM, RAM Pack CPU
* E P R O M R E A D * * * C O M P L E T E D ! * *			CLR		
1 . E C H K 2 . W R I T E 3 . V E R I F Y 4 . R E A D					EPROM Function

1) EPROM Read Error가									
* E P R O M R E A D * * * F A I L E D ! * *									

4.12

4.12.1 All Clear

Mode		
RUN/PAU	PGM	DEB
x		x

가

0 1 3 2 0

L O A D

0 1 3 2 0

L O A D

0 1 3 2 0

0 0 0 0 0

0 0 0 0 0

P G M A L L C L E A R

* * S u r e ? < E N T > * *

0 0 0 0 0

F U N (0 0 0) N O P

CLR

CLR

CLR

SHFT

DELT

ENT

Device M0024가

LOAD가

1) 00000 NOP

4.12.2 Scan Time

Mode																																										
RUN/PAU	PGM	DEB																																								
	x	x																																								
K200S, K300S, K1000S K10S, K30S, K60S		1 Scan Time 가																																								
1 Scan																																										
LOADER		KEY																																								
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>		0	0	0	0	0																L	O	A	D							M	0	0	0	0						<div>CLR CLR</div>
0	0	0	0	0																																						
L	O	A	D							M	0	0	0	0																												
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>		0	0	0	0	0																																				<div>MON</div>
0	0	0	0	0																																						
<table border="1"> <tr><td>M</td><td>I</td><td>N</td><td>.</td><td>C</td><td>U</td><td>R</td><td>.</td><td>M</td><td>A</td><td>X</td><td>.</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>0</td><td>0</td><td>0</td><td>3</td><td>0</td><td>0</td><td>0</td><td>4</td><td>0</td><td>0</td><td>0</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>		M	I	N	.	C	U	R	.	M	A	X	.									0	0	0	3	0	0	0	4	0	0	0	5									<div>CLR</div>
M	I	N	.	C	U	R	.	M	A	X	.																															
0	0	0	3	0	0	0	4	0	0	0	5																															
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> </table>		0	0	0	0	0																																				<div>STEP</div>
0	0	0	0	0																																						
<table border="1"> <tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr> <tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td></td><td></td><td></td><td></td><td></td><td>M</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td></tr> </table>		0	0	0	0	0																L	O	A	D							M	0	0	0	0						<div>RUN 0 Read</div>
0	0	0	0	0																																						
L	O	A	D							M	0	0	0	0																												
		<div>Scan</div>																																								
		<div>RUN 0 Read</div>																																								

1) Scan Time ms

4-76

4.12.3 RTC

Mode				
RUN/PAU	PGM	DEB		
		x		
			K10S/K30S/K60S RTC K200S A RTC B C	
LOADER			KEY	
<div>0 0 0 0 0</div> <div></div>			<div>CLR CLR</div> <div>SHFT T</div>	
<div>9 9 Y 0 8 M 1 0 D T U</div> <div>1 1 H 2 7 M 3 5 S</div>			<div>ENT</div>	
<div>9 9 Y 0 8 M 1 0 D T U</div> <div>1 1 H 2 7 M 3 5 S</div>			<div>TEST</div>	
<div>9 9 Y ? ? M 1 0 D T U</div> <div>1 1 H 2 7 M 3 5 S</div>			<div>0 OFF 9 ENT</div>	
<div>9 9 Y 0 9 M 1 0 D T U</div> <div>1 1 H 2 7 M 3 5 S</div>			<div>ENT TEST</div> <div>STEP STEP</div>	
<div>9 9 Y 0 9 M 1 0 D F R</div> <div>1 1 H 2 7 M 3 5 S</div>				

Mode			
RUN/PAU	PGM	DEB	
		x	
Hexa	Decimal	, Decimal	Hexa
LOADER		KEY	
<div>0 0 0 0 0</div> <div>LOAD NOT M 0 0 0 0</div> <div>FROM :</div> <div>FROM : 0 0 0 0 0 6 5 5 3 5</div> <div>TO : h 0 0 0 0 F F F F</div> <div>FROM :</div> <div>FROM : h 0 0 0 0 F F F F</div> <div>TO : 0 0 0 0 0 6 5 5 3 5</div> <div>0 0 0 0 0</div> <div>LOAD NOT M 0 0 0 0</div>		<div>SHFT AUX</div> <div>6 5 5</div> <div>3 5</div> <div>ENT</div> <div>CLR</div> <div>"B</div> <div>F F F F</div> <div>ENT</div> <div>CLR CLR</div> <div>STEP</div>	
		<div>가</div> <div>Decimal</div> <div>(Decimal Hex)</div> <div>65535 16 hFFFF</div> <div>Hexa</div> <div>(Hexa Decimal)</div> <div>16 hFFFF 10 65535</div>	

4.12.5 LCD Back Light On/Off

Mode

RUN/PAU

PGM

DEB

KLD-150S Handy Loader

LCD Back Light

On/Off

K10S/K30S/K60S

Loader

1.LCD2.BUZZER

3.PROGRAMMODE

LOADER

KEY

00000

LOADNOTMOOOO

SHIFT

PRM

H.LPARAMETER

1.LCD2.BUZZER

1

Loader

LCDBACKLIGHT

1.ON2.OFF

2

LCD Back Light

LCDBACKLIGHT

1.ON2.OFF

CLR

LCD Back Light Off

H.LPARAMETER

1.LCD2.BUZZER

CLR

STEP

Loader

00000

LOADNOTMOOOO

4.12.6 Buzzer

Mode																														
RUN/PAU	PGM	DEB																												
KLD-150S Handy Loader			Buzzer	On/Off																										
Buzzer																														
LOADER			KEY																											
<table><tr><td>0</td><td>0</td><td>0</td><td>0</td><td>0</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr><tr><td>L</td><td>O</td><td>A</td><td>D</td><td></td><td>N</td><td>O</td><td>T</td><td></td><td></td><td>M</td><td>O</td><td>O</td></tr></table>			0	0	0	0	0									L	O	A	D		N	O	T			M	O	O	<div>SHIFTPRM</div>	
0	0	0	0	0																										
L	O	A	D		N	O	T			M	O	O																		
<table><tr><td>H</td><td>.</td><td>L</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td></tr><tr><td>1</td><td>.</td><td>L</td><td>C</td><td>D</td><td></td><td>2</td><td>.</td><td>B</td><td>U</td><td>Z</td><td>Z</td><td>E</td></tr></table>			H	.	L	P	A	R	A	M	E	T	E	R		1	.	L	C	D		2	.	B	U	Z	Z	E	<div>2</div>	
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1	.	L	C	D		2	.	B	U	Z	Z	E																		
<table><tr><td></td><td>B</td><td>U</td><td>Z</td><td>Z</td><td>E</td><td>R</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td></tr><tr><td>1</td><td>.</td><td>O</td><td>N</td><td></td><td></td><td>2</td><td>.</td><td>O</td><td>F</td><td>F</td><td></td><td></td></tr></table>				B	U	Z	Z	E	R		S	E	T			1	.	O	N			2	.	O	F	F			<div>1</div>	
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	1	2	3	4	5																									
<table><tr><td></td><td>B</td><td>U</td><td>Z</td><td>Z</td><td>E</td><td>R</td><td></td><td>T</td><td>O</td><td>N</td><td>E</td><td></td></tr><tr><td></td><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>				B	U	Z	Z	E	R		T	O	N	E			1	2	3	4	5								<div>CLR</div>	
	B	U	Z	Z	E	R		T	O	N	E																			
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H	.	L	P	A	R	A	M	E	T	E	R																			
1	.	L	C	D		2	.	B	U	Z	Z	E																		
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0	0	0	0	0																										
L	O	A	D		N	O	T			M	O	O																		

K10S/K30S/K60S													Loader			
1	.	L	C	D		2	.	B	U	Z	Z	E	R			
3	.	P	R	O	G	R	A	M		M	O	D	E			

Loader																
Buzzer																
Buzzer Default '3'																
Buzzer																
Loader																

1) Loader	Off	On/Off가	Default																																																			
<table><tr><td></td><td>B</td><td>U</td><td>Z</td><td>Z</td><td>E</td><td>R</td><td></td><td>S</td><td>E</td><td>T</td><td></td><td></td></tr><tr><td>1</td><td>.</td><td>O</td><td>N</td><td></td><td></td><td>2</td><td>.</td><td>O</td><td>F</td><td>F</td><td></td><td></td></tr></table>		B	U	Z	Z	E	R		S	E	T			1	.	O	N			2	.	O	F	F			→	<table><tr><td></td><td>B</td><td>U</td><td>Z</td><td>Z</td><td>E</td><td>R</td><td></td><td>T</td><td>O</td><td>N</td><td>E</td><td></td></tr><tr><td></td><td>1</td><td>2</td><td>3</td><td>4</td><td>5</td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></tr></table>		B	U	Z	Z	E	R		T	O	N	E			1	2	3	4	5							
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1	.	O	N			2	.	O	F	F																																												
	B	U	Z	Z	E	R		T	O	N	E																																											
	1	2	3	4	5																																																	

4.12.7 Default

Mode																															
RUN/PAU	PGM	DEB																													
x		x																													
Default			가 Default																												
LOADER			KEY																												
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>.</td><td>L</td><td>A</td><td>T</td><td>C</td><td>H</td><td>A</td><td>R</td><td>E</td><td>A</td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R			1	.	L	A	T	C	H	A	R	E	A			<div>PRM</div>	Default
P	L	C	P	A	R	A	M	E	T	E	R																				
1	.	L	A	T	C	H	A	R	E	A																					
			<div>SHFT</div> <div>DELT</div>																												
<table border="1"> <tr><td>*</td><td></td><td></td><td>D</td><td>e</td><td>f</td><td>a</td><td>u</td><td>i</td><td>t</td><td></td><td>*</td><td></td></tr> <tr><td>*</td><td></td><td>P</td><td>R</td><td>M</td><td>W</td><td>r</td><td>i</td><td>t</td><td>e</td><td>?</td><td>*</td><td></td></tr> </table>			*			D	e	f	a	u	i	t		*		*		P	R	M	W	r	i	t	e	?	*		<div>ENT</div>		
*			D	e	f	a	u	i	t		*																				
*		P	R	M	W	r	i	t	e	?	*																				
<table border="1"> <tr><td>P</td><td>L</td><td>C</td><td>P</td><td>A</td><td>R</td><td>A</td><td>M</td><td>E</td><td>T</td><td>E</td><td>R</td><td></td><td></td></tr> <tr><td>1</td><td>.</td><td>L</td><td>A</td><td>T</td><td>C</td><td>H</td><td>A</td><td>R</td><td>E</td><td>A</td><td></td><td></td></tr> </table>			P	L	C	P	A	R	A	M	E	T	E	R			1	.	L	A	T	C	H	A	R	E	A				
P	L	C	P	A	R	A	M	E	T	E	R																				
1	.	L	A	T	C	H	A	R	E	A																					

4.13 MASTER - K

No.						
			K10S/K30S/ K60S/K100S	K200S	K300S	K1000S
1		EPROM Write/Program				
		Version				
2						
		10 Off				
3		Mode Change				
4		Password				
5		Password				
6		Program				
7		Program Read				
8		Program Insert				
9		Program Delete				
10		Device Search()				
11		Instruction Search				
12		Step Monitor				
13		Monitor				
14		Word Monitor				
15						
16		Word On/Off				
17		Word				
18		Timer/Counter				
19		Timer/Counter Monitor				
20		Timer/Counter				

No.					
		K10S/K30S/ K60S/K100S	K200S	K300S	K1000S
21	Timer/Counter On/Off				
22	Timer/Counter				
23	HSC Data Setting		/	/	/
24	HSC Default PRM Write		/	/	/
25	Latch Area Setting	/			
26	W.D.T	/			
27	Error Mode Setting	/			
28	I/O Table	/			
29	Link Parameter	/			
30	Link Control	/			
31	Timer Set	/			
32	Interrupt	/			
33	Baud Rate				
34	I/O Table Monitor	/			
35	Default Parameter Write				
36	1 Step Run	/			
37	Step Break Run	/			
38	Scan Run	/			
39	Value Break Run	/			
40	Program Clear				
41	Program Data Clear				
42	Block Data Change				
43	Block Move, Copy				
44	Program All Clear				

5.1 MASTER-K10/K60H/K200H (F05 Word)

		CPU			K10	K60H	K200H	K250
OS ROM CHK.SUM. Error	10		ROM Check Sum	A/S				
Memory Error	11		RAM	A/S				
Gate Array Error	12		LSI가	A/S				
24V Down Error	13		, 24V	A/S				
I/O Slot Error	14		I/O I/O	가 I/O				
WDT Over	20			WDT				
Parameter CHK.SUM Error	21		Check Sum					
Code Error	23		CPU가 가	Step ROM ROM				
Missing End Error	24		END	END (JMP-JME)				
Missing RET Error	25		RET	RET (JMP-JME)				
Operation Error	26	()	BCD 0 ~ 9 Digit가 Operand	Step				
I/O Check Error	27		Run I/O I/O 가	I/O				
Lower Battery Voltage	28							
Fuse (K250 Only)	29	()	가 ,	Fuse LED Fuse				

5.2 MASTER-K500H/K1000H (F06 Word)

		CPU		
system error	H1001		ROM H/W	A/S
Memory Destroyed	H1002		RAM	A/S
Gate Array Destroyed	H1003		LSI가	A/S
Sub Rack Power Down Error	H1004		Rack Power가 Down	Rack Power .
I/O Slot Verify Error	H2001		I/O I/O	I/O 가
Fuse Break Error	H2002	()	, 가	Fuse LED Fuse
Spectral Interface Error	H2003		Interface Error	A/S
WDT Over	H2004			WDT
Operation Error	H2005	()	BCD 0 ~ 9 가 Oprerand	
Parameter CHK. SUM Error	H3001		Check-Sum	
Maximum I/O Over	H3002		I/O 512 I/O	I/O
Instruction Code Error	H3003		CPU가 가	Step ROM ROM
Missing End Program	H3004		END	END (JMP-JME)
Missing RET Program	H3005		RET	I/O
I/O Verify Error	H3006	()	Run I/O 가 (): I/O	
Battery Error	H3007			A/S

() 가

		CPU		
DLU Interface Error	H4001		DLU Interface H/W	A/S
DLU Parameter Setting	H4002		DLU parameter가	DLU parameter .
DLU Failure	H4003		DLU Link Error	DLU .
DLU Self-Check Error	H4004		DLU Error Check.	DLU .
DLU Parameter Error	H4005		Run DLU Parameter Error	DLU .
RMU1 Interface Error	H4011		RMU1 interface H/W	A/S
RMU1 Parameter Setting	H4012		RMU1 Parameter가	RMU1 parameter .
RMU1 Failure	H4013		RMU1 Link Error	RMU1 .
RMU1 Self-Check Error	H4014		Run Self-Check Error	RMU1 .
RMU1 Parameter Error	H4015		Run RMU1 Parameter Error	RMU1 .
RMU2 Interface Error	H4021		RMU2 interface H/W	A/S
RMU2 Parameter Setting	H4022		RMU2 Parameter가	RMU2 parameter .
RMU2 Failure	H4023		RMU2Link Error	RMU2 .
RMU2 Self-Check Error	H4024		RMU2 Self-Check Error	RMU2 .
RMU2 Parameter Error	H4025		Run RMU2 Parameter Error	RMU2 .
RMU3 Interface Error	H4031		RMU3 interface H/W	A/S

		CPU		
RMU3 Setting Error	H4032		RMU3 Parameter가	RMU3 parameter .
RMU3 Failure	H4033		RMU3Link Error	RMU3 .
RMU3 Self-Check Error	H4034		RMU3 Self-Check Error	RMU3 .
RMU3 Parameter Error	H4035		Run RMU3 Parameter Error	RMU3 .

5.3 MASTER-K10S/K30S/K60S/K100S(F05 Word)

		CPU		
I/O Error	H0014			Off/On
Code Error	H0023			
Parameter Error	H0021			
Missing END Error	H0024		END	END
Missing RET Error	H0025		RET	RET

5.4 MASTER-K80S/K200S/K300S/K1000S(F06 Word)

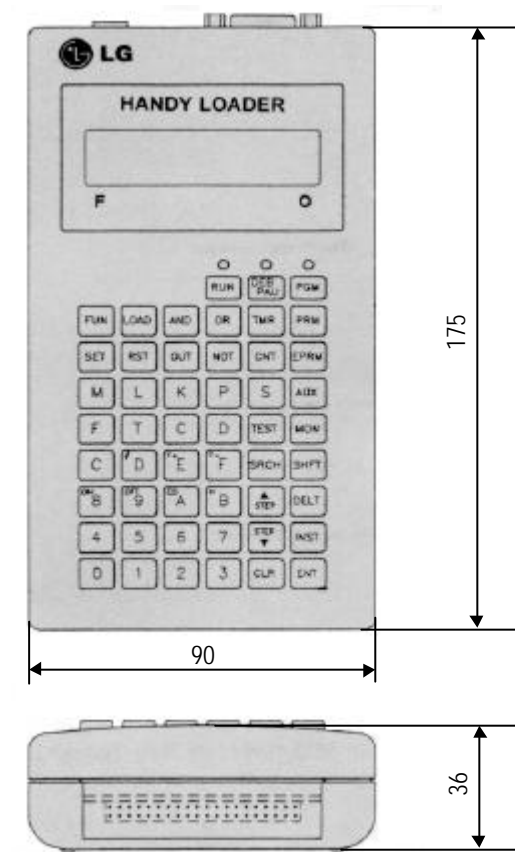
			CPU		
System Error	System Error	0001h		ROM H/W	A/S
OS ROM Error	OS ROM Error	0002h		ROM	A/S
OS RAM Error	OS RAM Error	0003h		RAMD L	A/S
Data RAM Error	Data RAM Error	0004h		Data RAM	A/S
Program RAM Error	PGM RAM Error	0005h		Program RAM	A/S
Gate Array Error	G/A Error	0006h		LSI가	A/S
SUB Rack Power Down Error	SUB Power Error	0007h		RACK Power가 Down	RACK Power .
OS WDT Error	OS WDT Error	0008h		CPU O/S Watch Dog Timer Error	Power Off A/S
RAM Error	Common RAM Error	0009h		RAM I/F Error	A/S
Fuse Break Error	I/O Fuse Error	000Ah	()	가 ,	LED
Instruction Code Error	OP Code Error	000Bh		CPU가 () 가	A/S
Flash Mem. Error()	User Mem Error	000Ch		Flash Memory가 Read, Write	Flash Memory
I/O Slot Error	I/O Slot Error	0010h		I/O I/O	. I/O
Maximum I/O Over	MAX I/O Over	0011h		I/O Over 가 I/O	I/O

			CPU		
Special Interface	Special I/F Error	0012h		Interface Error	A/S
FMM 0 I/F Error	FMM 0 I/F Error	0013h		FMM 0 I/F Error	A/S
FMM 1 I/F Error	FMM 1 I/F Error	0014h		FMM 1 I/F Error	A/S
FMM 2 I/F Error	FMM 2 I/F Error	0015h		FMM 2 I/F Error	A/S
FMM 3 I/F Error	FMM 3 I/F Error	0016h		FMM 3 I/F Error	A/S
Parameter Error	Parameter Error	0020h		Check-Sum	
I/O Parameter Error	I/O PARA Error	0021h	()	Run I/O I/O 가	I/O
Maximum I/O Over	I/O PARA Over	0022h		I/O I/O 가 I/O	
FMM 0 Parameter Error	FMM 0 PARA Error	0023h		FMM 1 Parameter Error	
FMM 1 Parameter Error	FMM 1 PARA Error	0024h		FMM 2 Parameter Error	
FMM 2 Parameter Error	FMM 2 PARA Error	0025h		FMM 3 Parameter Error	
FMM 3 Parameter Error	FMM 3 PARA Error	0026h		FMM 4 Parameter Error	
Operation Error	Operation Error	0030h	()	BCD 0 ~ 9 Digit가 Operand	
WDT Over	WDT Over Error	0031h			
Run Program Check Error	PGM Change Error	0032h		Run Error가 (No SBRT, JME, END...)	Run 가 (JMP ~ JME, FOR ~ NEXT, CALLx, SBRTx...)
Program Check Error	PGM Check Error	0033h		Error가	A/S

			CPU		
Code Check Error	Code Check Error	0040h		CPU가 가	
Missing End Program	Miss End Error	0041h		End	END (JMP ~ JME)
Missing RET Program	Miss RET Error	0042h		RET	RET (JMP ~ JME)
Missing SBRT Program	Miss SBRT Error	0043h		SBRT	SBRT (JMP ~ JME)
JMP ~ JME Error	JMP(E) Error	0044h		JMP ~ JME Error	JMP ~ JME
FOR ~ NEXT Error	FOR ~ NEXT Error	0045h		FOR ~ NEXT Error	FOR ~ NEXT
MCS ~ MCSCLR instruction Error	MCS ~ MCSCLR Error	0046h		MCS ~ MCSCLR Error	MCS ~ MCSCLR
MPUSH ~ MPOP Error	MPUSH ~ MPOP Error	0047h		MPUSH ~ MPOP Error	MPUSH ~ MPOP
Dual Coil Error	Dual Coil Error	0048h		Timer, Counter	Timer, Counter
Syntax Error	Syntax Error	0049h		Load, And, (or)Load	
Battery Error	Battery Error	0050h			

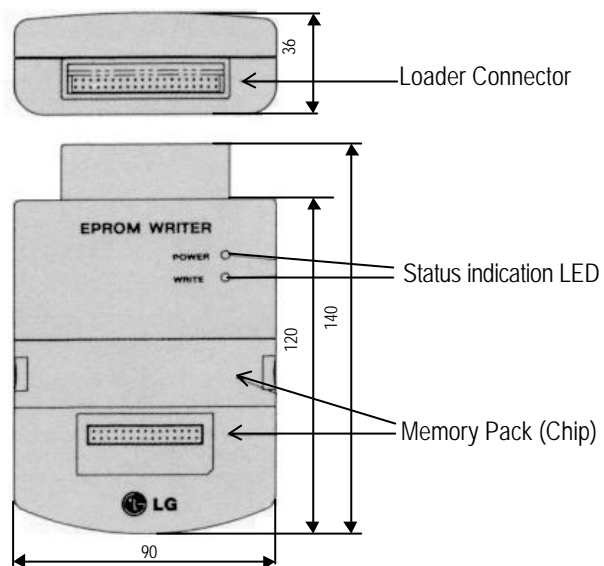
6.1 KLD-150S

(: mm)



6.2 KEW-150S

(: mm)



Handy Loader (KLD-150S)	
Handy Loader	Off

1. Code (Function Code)

Function No.	0	1	2	3	4	5	6	7	8	9
00x	NOP	END	STC	CLC	RET	MPUSH	MLOAD	MPOP	STOP	CLE
01x	MCS	MCSCLR	JMP	JME	CALL	CALLP	SBRT	D	DNOT	
02x	INC	INCP	DINC	DINCP	DEC	DECP	DDEC	DDECP	LD=	LDD=
03x	ROL	ROLP	DROL	DROLP	ROR	RORP	DROR	DRORP	LD>	LDD>
04x	RCL	RCLP	DRCL	DRCLP	RCR	RCRP	DRCR	DRCRP	LD<	LDD<
05x	CMP	CMPP	DCMP	DCMPP	TCMP	TCMPP	DTCMP	DTCMPP	LD>=	LDD>=
06x	BCD	BCDP	DBCD	DBCDP	BIN	BINP	DBIN	DBINP	LD<=	LDD<=
07x	WSFT	WSFTP	MULS	MULSP	BSFT	BSFTP	DMULS	DMULSP	LD<>	LDD<>
08x	MOV	MOVP	DMOV	DMOVP	CMOV	CMOVP	DCMOV	DCMOVP	DIVS	DIVSP
09x	GMOV	GMOVP	FOMV	FOMVP	AND=	ANDD=	AND>	ANDD>	AND<	ANDD<
10x	BMOV	BMOVP	XCHG	XCHGP	DXCHG	DXCHGP	AND>=	ANDD>=	AND<=	ANDD<=
11x	ADD	ADDP	DADD	DADDP	SUB	SUBP	DSUB	DSUBP	AND<>	ANDD<>
12x	MUL	MULP	DMUL	DMULP	DIV	DIVP	DDIV	DDIVP	DDIVS	DDIVSP
13x	ADDB	ADDBP	DADDB	DADDBP	SUBB	SUBBP	DSUBB	DSUBBP	PIDTUN	PIDCAL
14x	MULB	MULBP	DMULB	DMULBP	DIVB	DIVBP	DDIVB	DDIVBP	PID8AT	PID8
15x	WAND	WANDP	DWAND	DWANDP	WOR	WORP	DWOR	DWORP	RCV	SEND
16x	WXOR	WXORP	DWXOR	DWXORP	WXNR	WXNRP	DWXNR	DWXNRP	RCV	SND
17x	BSUM	BSUMP	DBSUM	DBSUMP	SEG	SEGP	ENCO	ENCOP	DECO	DECOP
18x	FILR	FILRP	DFILR	DFILRP	FILW	FILWP	DFILW	DFILWP	OR=	ORD=
19x	ASC	ASCP	UNI	UNIP	DIS	DISP	OR>	ORD>	OR<	ORD<
20x	IORF	IORFP	WDT	WDTP	FALS	DUTY	FOR	NEXT	OUTOFF	
21x	HSCNT	DIN	DINP	DOUT	DOUTP	HSC	OR>=	ORD>=	OR<=	ORD<=
22x	BREAK	EI	DI	BSET	BRST	IRET	TDINT	INT	OR<>	ORD<>
23x	GET	GETP	RGET	RPUT	PUT	PUTP	BOUT	SR	EI	DI
24x	NEG	NEGP	DNeg	DNegP	READ	WRITE	CONN	STATUS	BLD	BLDN
25x	BAND	BANDN	BOR	BORN	PLSOUT	SND8	MODBUS			

: K100S, K300S, K200S 가
 : K10S, K10S1, K30S, K60S 가
 : K200S(B/C) 가
 : K10S, K10S1, K30S, K60S, K200S(C) 가
 : K80S 가